

NEREID General Workshop 1, April 12 - 13, 2016.

Location: Maison Minatec, 3 parvis Louis Neel, 38054 Grenoble - France.



DAY 1 - Tuesday, April 12			
9:00-9:30	Badge registration and welcome coffee		
	Room 221		Room 222 and 223
9:30-9:45	Introduction by Francis Balestra		
09:45-12:45	Session 1: Application Domains Vision Overview (plenary)		10:45-11:05 common coffee break
	chairman Patrick Corsi, Cayak		
12:45-14:00	Petit Salon Lunch		
	Room 221		Room 222 and 223
14:00-17:45	Session 2: Technology Areas Synthesis by Task-WP Leaders (plenary)		16:00 common coffee break
	chairman Patrick Corsi, Cayak		
18:00-21:00	Petit Salon Oenological session and cocktail dinner		

DAY 2 - Wednesday, April 13			
08:00-09:00	Merged rooms 223-222	1st meeting of Steering Committee (WP and task leaders)	
09:15-09:25	Room 221	Introduction to Session 3 by Enrico Sangiorgi	
09:25-12:45	Parallel session	Parallel session	Parallel session
	Room 221 (split)	Room 222	Room 223
	Session 3-A: WP2 (Beyond CMOS) + WP3 task 3.1 (nanoscale FET)	Session 3-B: WP3 task 3.2 (Connectivity)	Session 3-C: WP4 task. 4.1 (Smart Sensors)
	chairman Francis Balestra, Grenoble INP	chairman Jean-Baptiste DAVID, CEA	chairman Hoel Guerin, EPFL
			Session 3-D: WP4 task 4.2 (Smart Energy)
			chairman Gaudenzio Meneghesso, IUNET
			Session 3-E: WP5 (System Design and Heterogeneous)
			chairman Danilo Demarchi, POLITO
			Session 3-F: WP6 (Equipment and Manufacturing Science)
			chairman Markus Pfeffer, Fraunhofer
12:45-14:00	Petit Salon Lunch		
	Room 221		Room 222
14:00-15:40	Session 4: Cross synthesis (plenary)		
	chairman Patrick Corsi, Cayak		
15:40-16:00	Synthesis and next steps (plenary) by Francis Balestra		16:00 Common coffee and closure
16:00	End of General Workshop 1		