



SiNANO-NEREID Workshop:

Equipment and Manufacturing Science

Leuven, September 11th, 2017

WP6

Technologies/concepts covered by the Roadmap

- ❖ Difficult to select materials and equipment
Rely on the output of other WPs

- ❖ Emerging technologies beyond CMOS

This Workshop: Strengthen efforts in 2D materials
(TFETs, optoelectronics, topological structures,)
Requires manufacturing and tooling

Large area growth of 2D Materials for Nanoelectronics

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Acknowledgements

Collaborators

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IMEC I. Radu, M. Caymax

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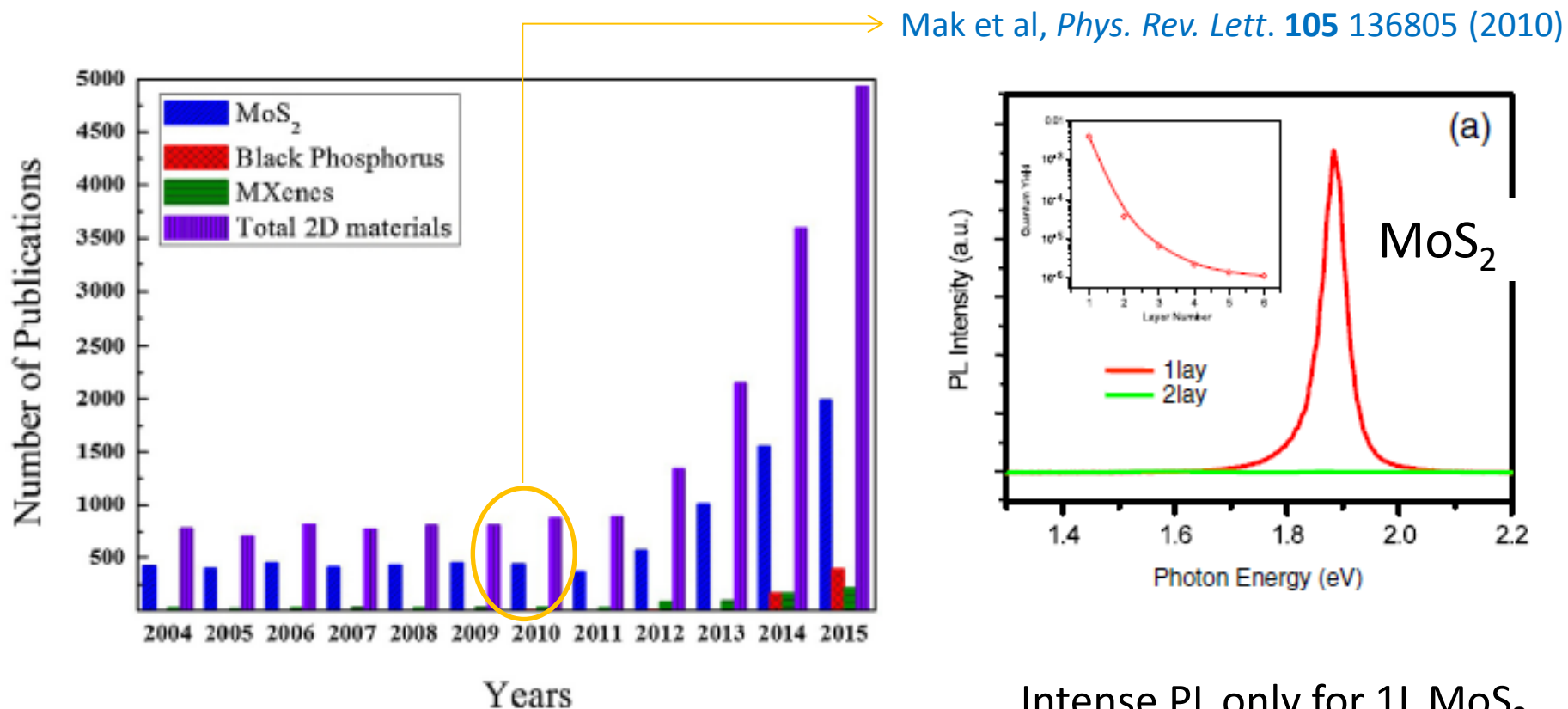
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(Labex LANEF du Programme d'Investissements d'Avenir)
- Equipex ANR-11-EQPX-0010 (GR) (G. Renaud)

Outline

- **2D materials basics & applications**
- **Scalable growth of 2D materials**
VPD, CVD, MOCVD
- **MBE-grown 2D materials**
 - Structural
 - Electronic band structure,
 - W. d. Waals heterostructures

2D Materials research history

2D Metal Dichalcogenides



Source: American Chemical Society database
<https://www.acs.org/content/acs/en.html> (2016)

Intense PL only for 1L MoS₂
1L → 2L : Direct → Indirect

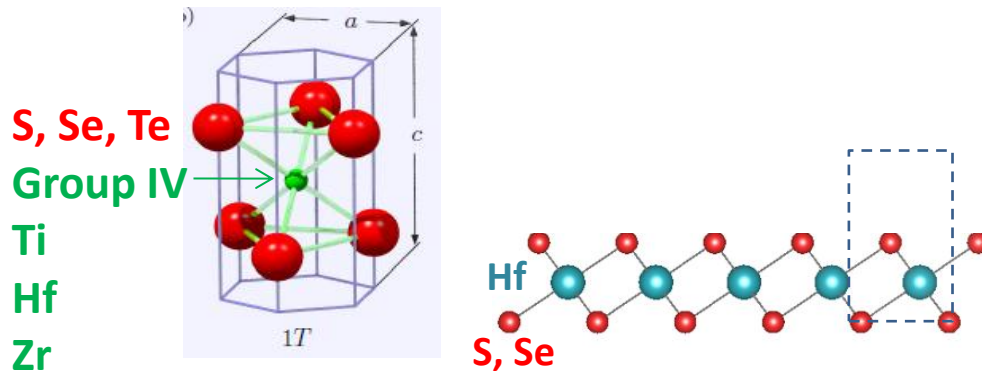
2D dichalcogenide materials

Large variety of physical properties and crystal structures

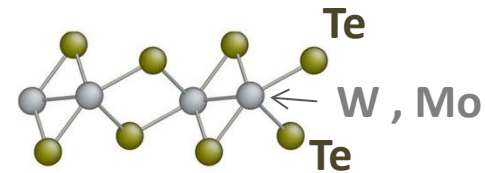
semiconductors	(Mo, W)S ₂ , <u>(Mo, W)Se₂</u> , (Hf, Zr)Se ₂ , <u>SnSe₂</u>
metals	TaSe ₂ , TiSe ₂
semimetals	<u>HfTe₂</u> , <u>ZrTe₂</u>
superconductors	NbSe ₂
Weyl semimetals	WTe ₂ , <u>MoTe₂</u>
topological insulators	Bi ₂ Se ₃ , Bi ₂ Te ₃ monolayer WTe ₂ , <u>MoTe₂</u>

Crystal Structure

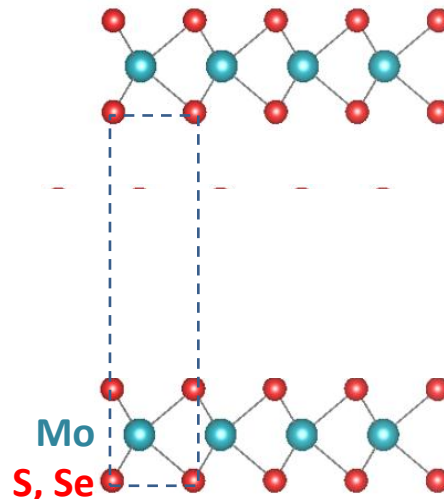
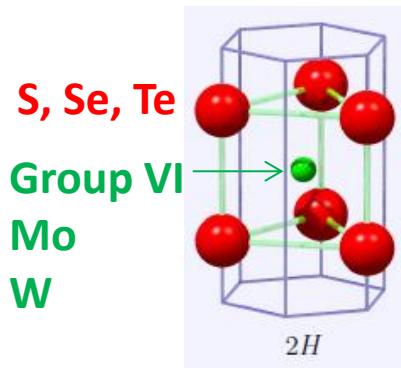
1T octahedral



Distorted 1T'



2H prismatic



MoTe₂ @ RT

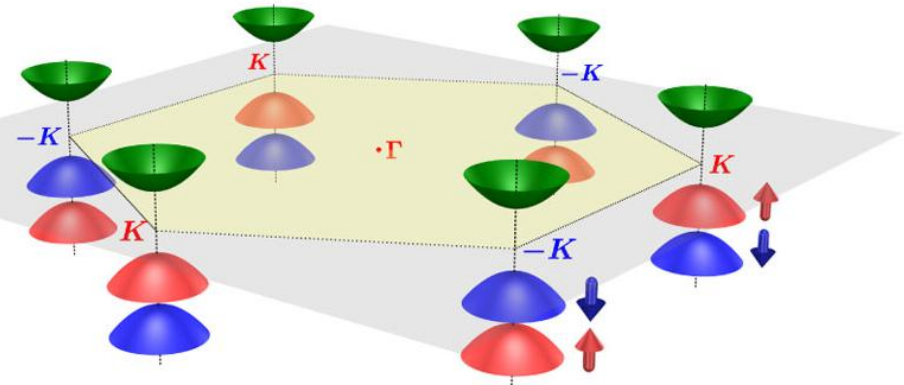
- 2H semiconductor stable
- 1T' metallic metastable

Possibility for
Semiconductor to metal
transition

1 ML MoS₂: Broken Inversion Symmetry

D. Xiao et al., *Phys. Rev. Lett.* **108**, 196802 (2012)

- Spin-Valley coupling
Spin polarized transport
“Valleytronics”

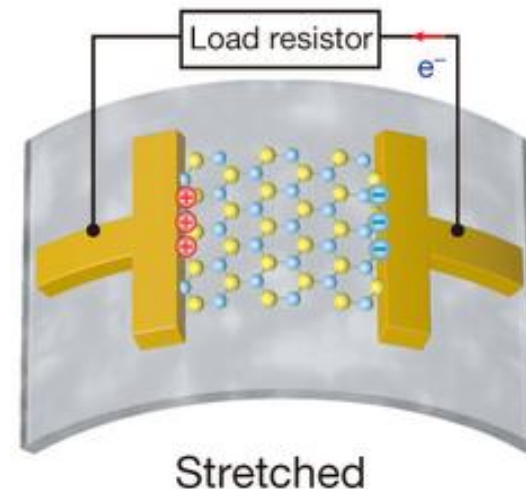


K.F. Mak et al., *Nat. Nanotech.* **7**, 494 (2012)

- Control valley spin polarization
by circularly polarized light

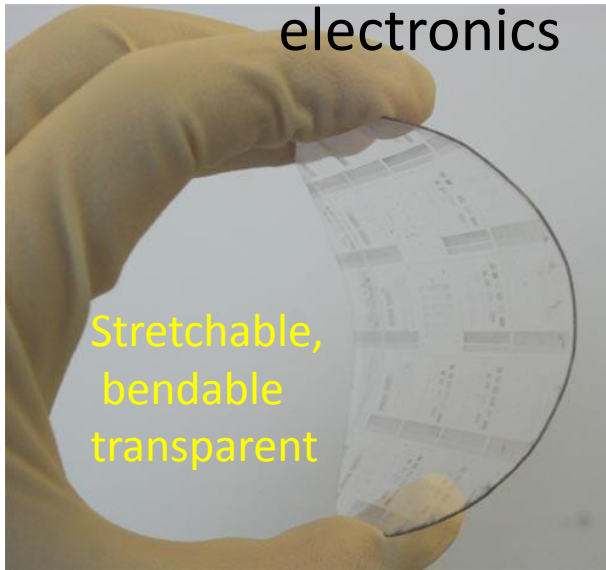
W. Wu et al., *Nature* **514**, 470 (2014)

- Piezoelectricity in MoS₂
for energy conversion



Benefits of Atomically Thin Layers

Flexible, transparent
electronics



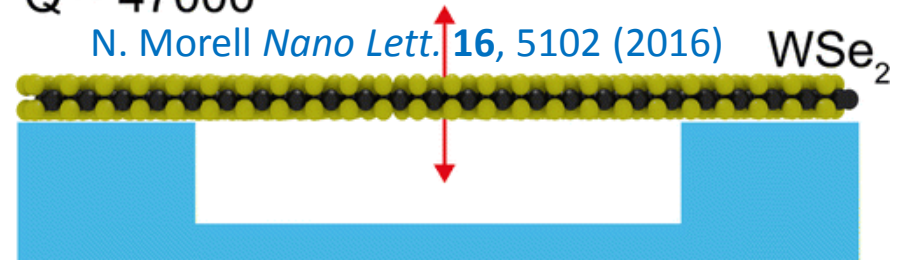
Stretchable,
bendable
transparent

High mechanical strength

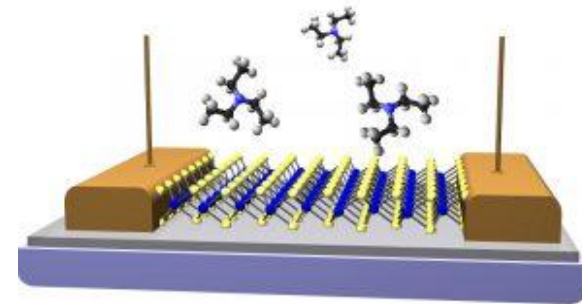
Electromechanical resonators

$Q \sim 47000$

N. Morell *Nano Lett.* **16**, 5102 (2016)



Sensitive chemical sensors

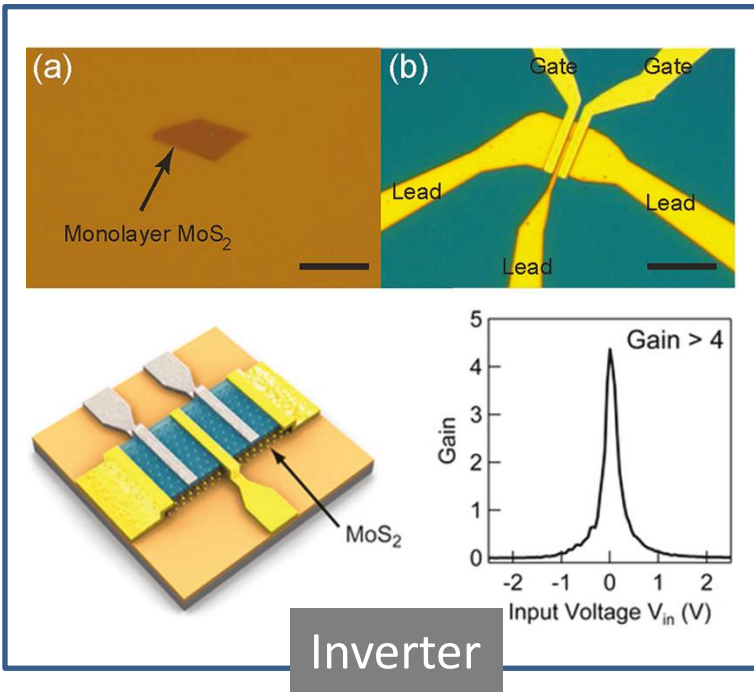


High surface to volume ratio

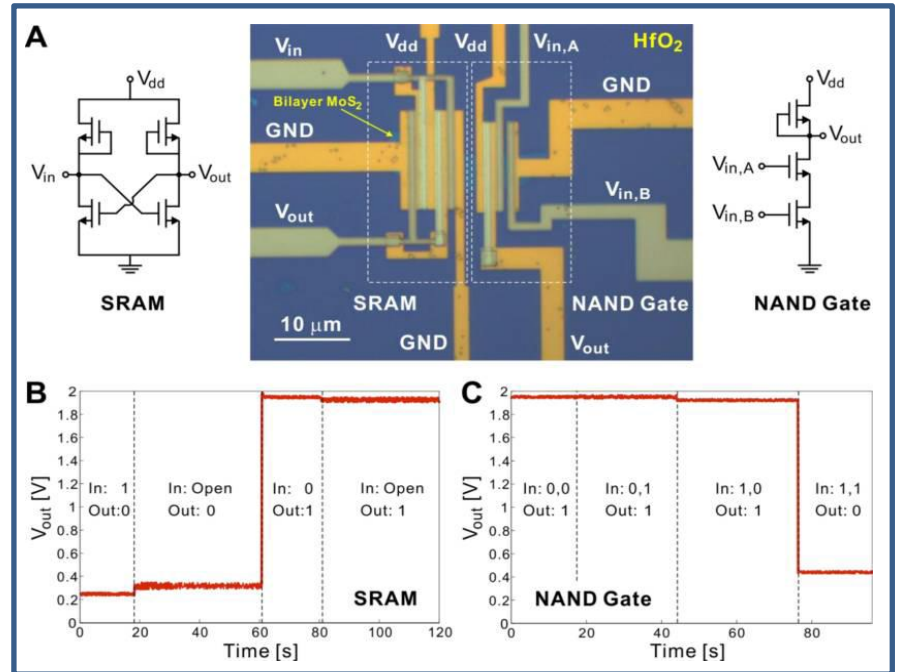
F.K. Perkins *Nano Lett.*, **13** 668 (2013)

MoS₂ Devices and Circuits

EPFL: Radisavleivic, Kis, et al.,
ACS Nano **12**, 9934 (11)



MIT: H. Wang, T. Palacios, et al.,
Nano Lett. **12**, 4674 (12)

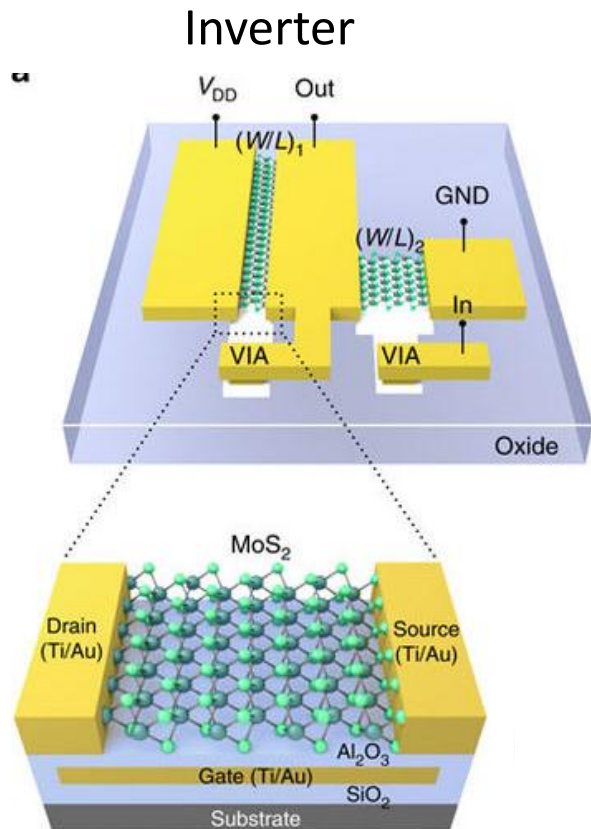


- Large potential to impact nanoelectronics

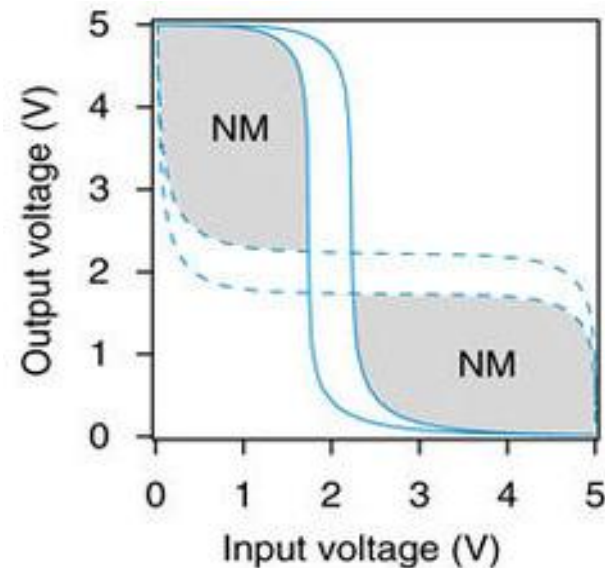
Microprocessor based on a 2D semiconductor

S. Wachter et al., *Nature Communications* **8**, 14948 (2017)

Scalable 1-bit microprocessor with 115 transistors
made of CVD MoS₂ (2ML)



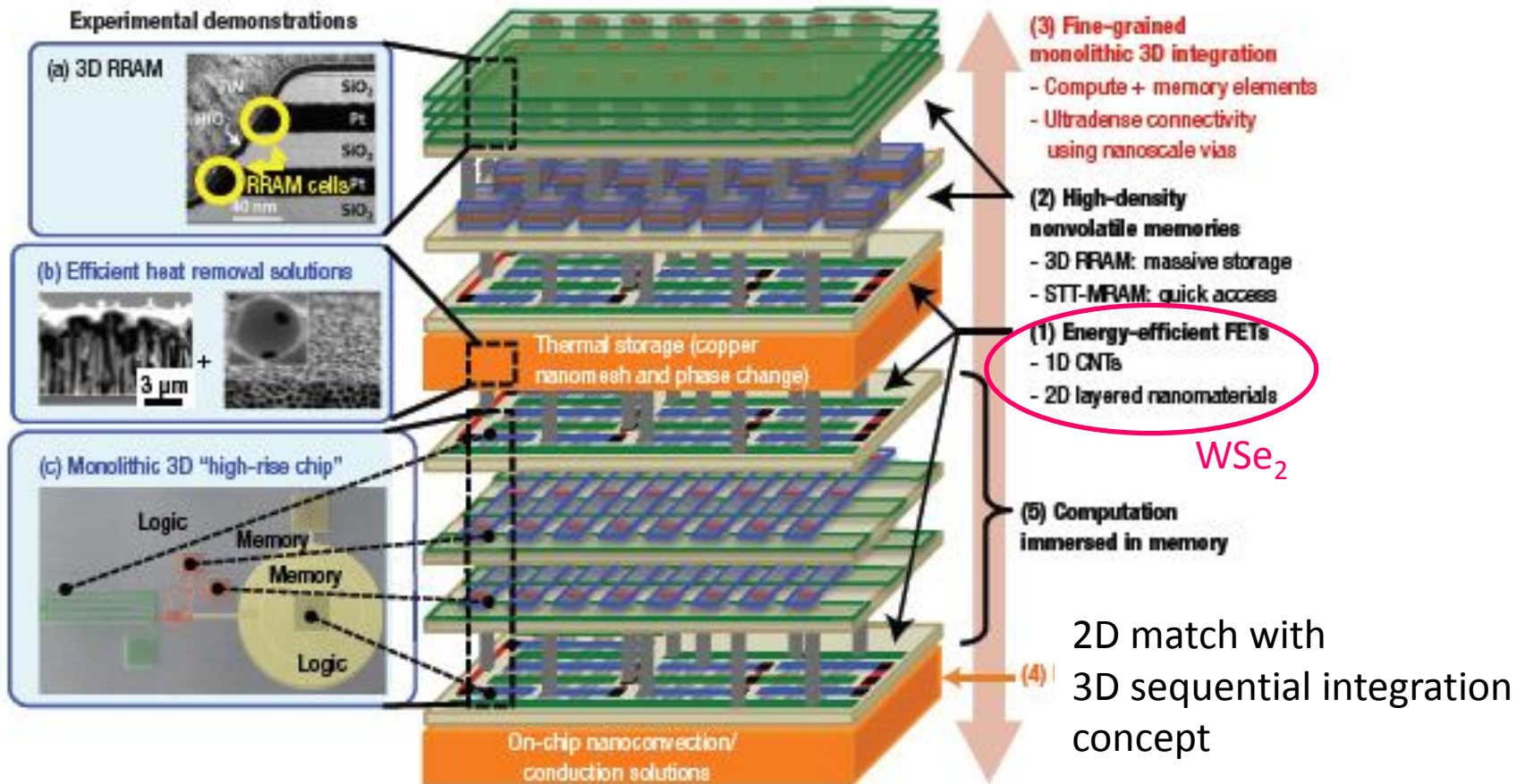
Voltage gain ~ 60
SNM $\sim 0.59 \times (V_{dd}/2)$



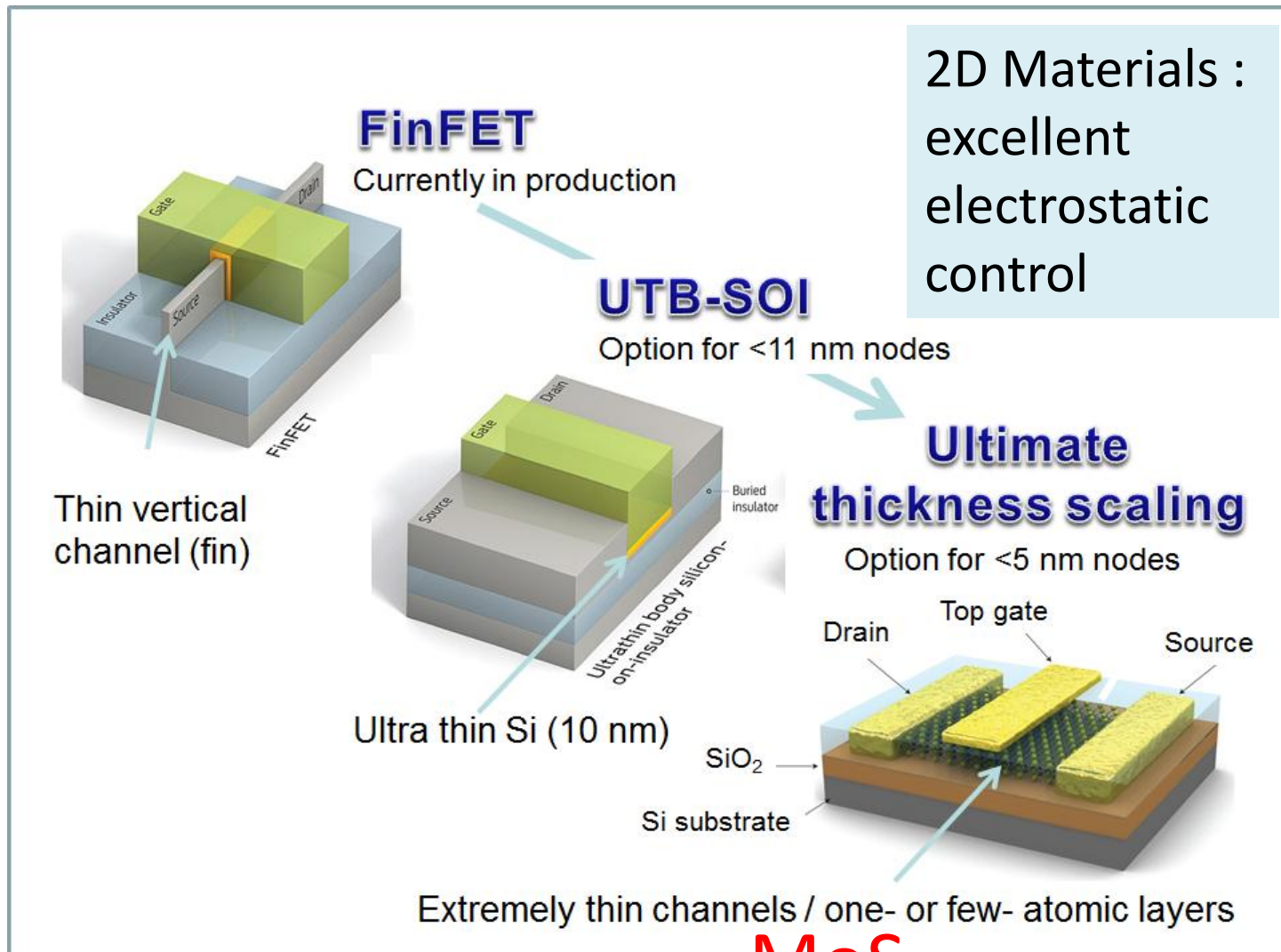
Re-Booting Computer

Energy-Efficient Abundant-Data Computing: The N3XT 1,000X

M.M.S Aly et al., *IEEE Computer* **48**, 24 (2015)



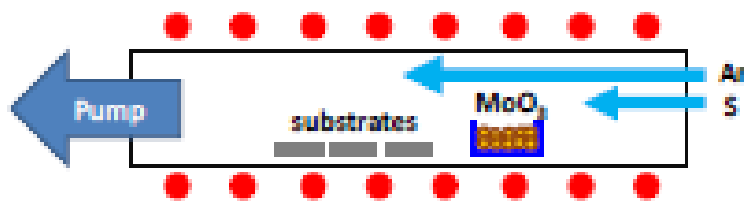
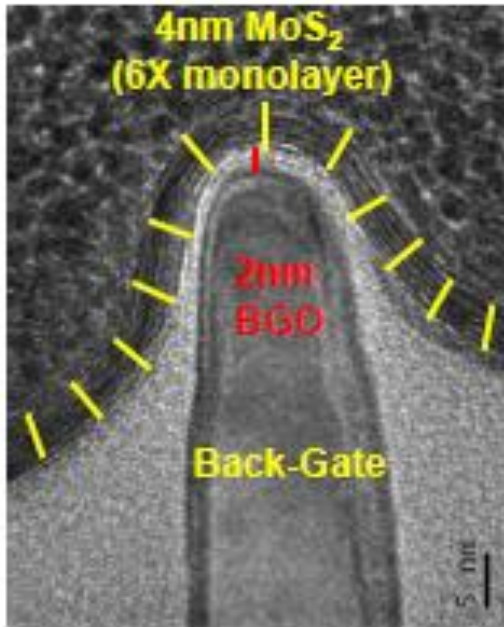
Device Scaling



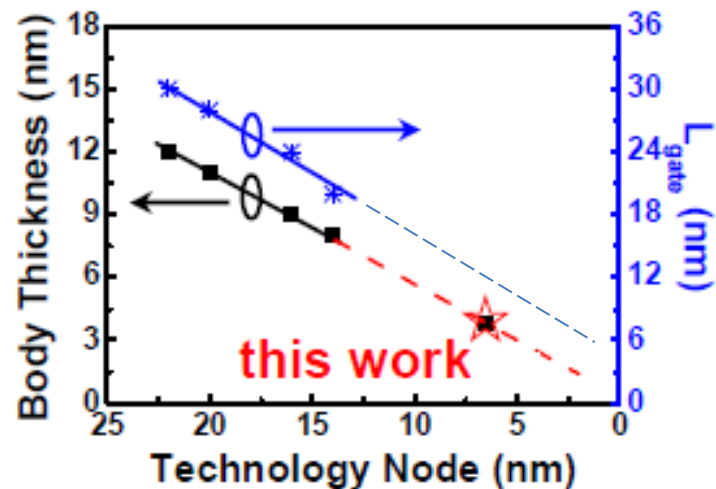
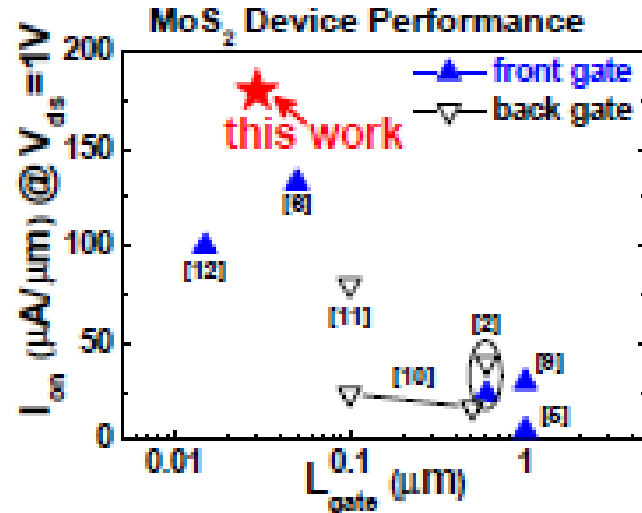
First CVD MoS₂ FinFET

Back gated → reconfigurable between low power/high performance

M.-C Chen et al., *IEDM* 2015

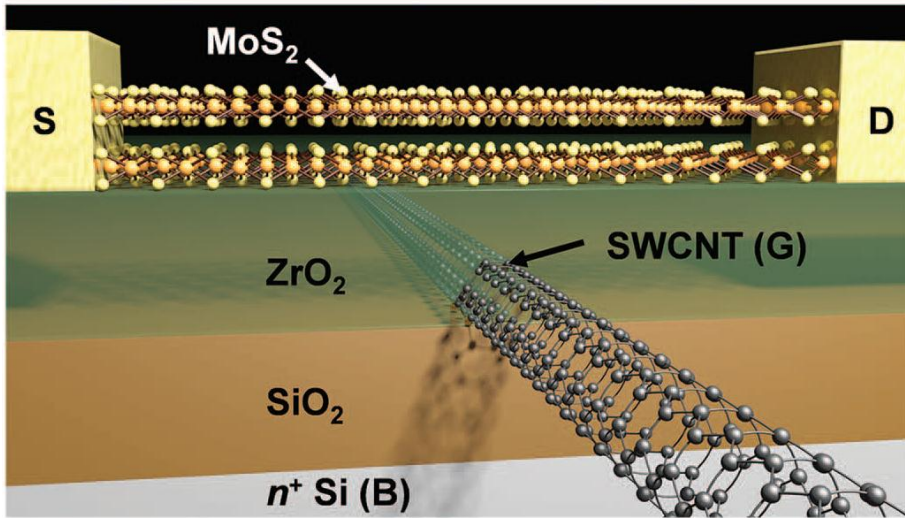


MoS₂ by CVD



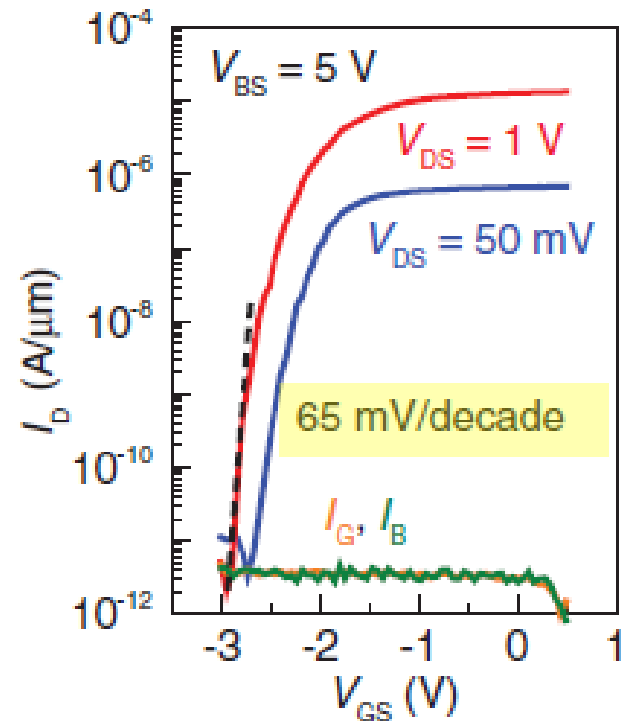
MoS₂ FET with 1 nm Gate Length

S. B. Desai *Science* **354**, 6308 (2016)



Simulations :
Could impact low power technology
2 orders of magnitude better than Si
in OFF state leakage

Ideal switching characteristics



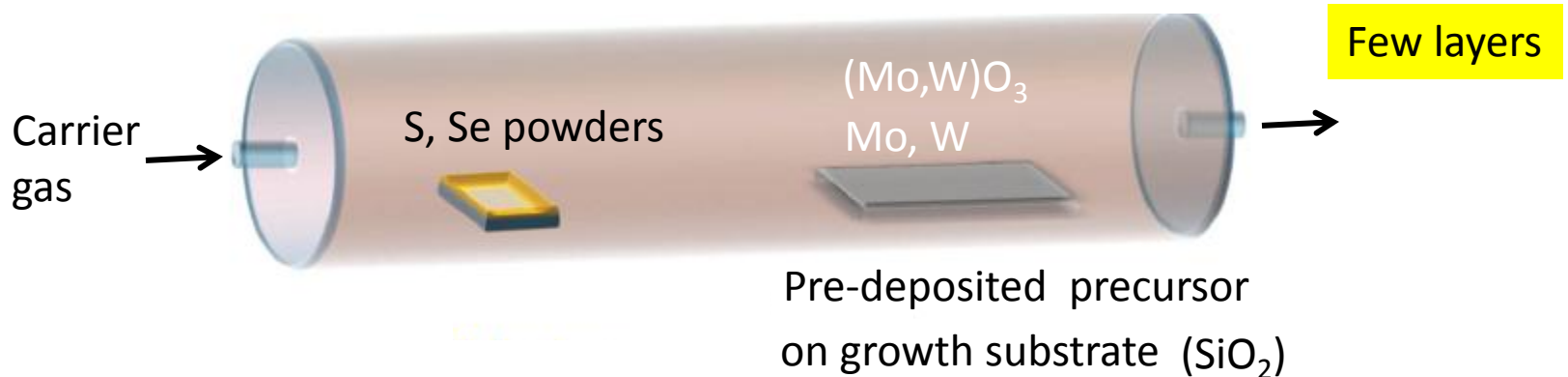
Realized on flakes

Scalable synthesis of 2D materials

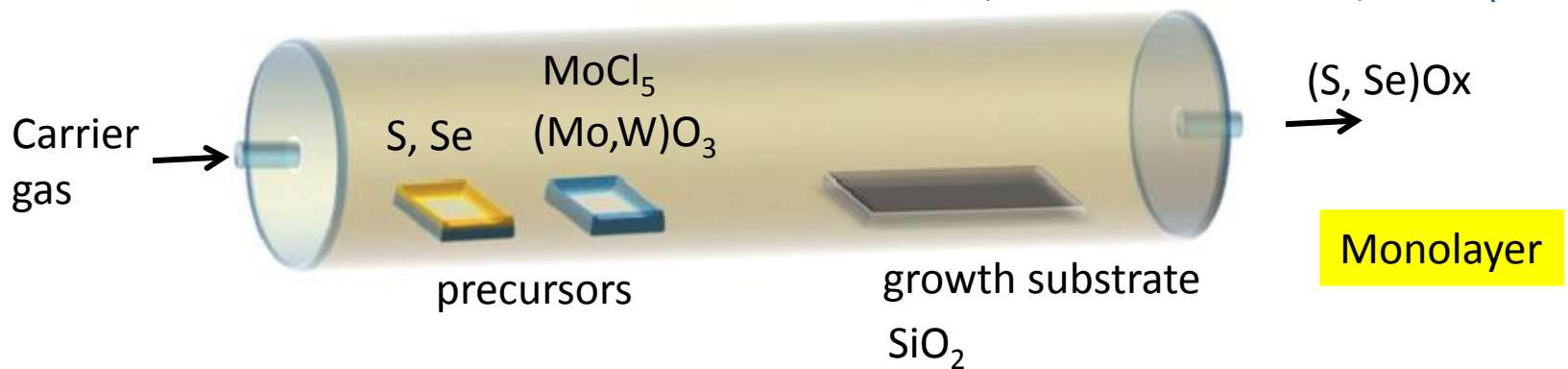
- Vapor Phase Deposition-sulphurization
- Chemical Vapor Deposition
- Metalorganic Vapor Phase deposition
- Atomic Layer Deposition
- Sputtering
- Thermal Decomposition
- Molecular Beam Epitaxy

Chemical Vapor Deposition

- VPD from solid sources –sulphurization



- CVD from all-solid sources [Y.-H. Lee et al, *Adv. Mater.* **24**, 2320 \(2012\)](#)
[Review Y Shi et al., *Chem. Soc. Rev.* **44**, 2744 \(2015\)](#)

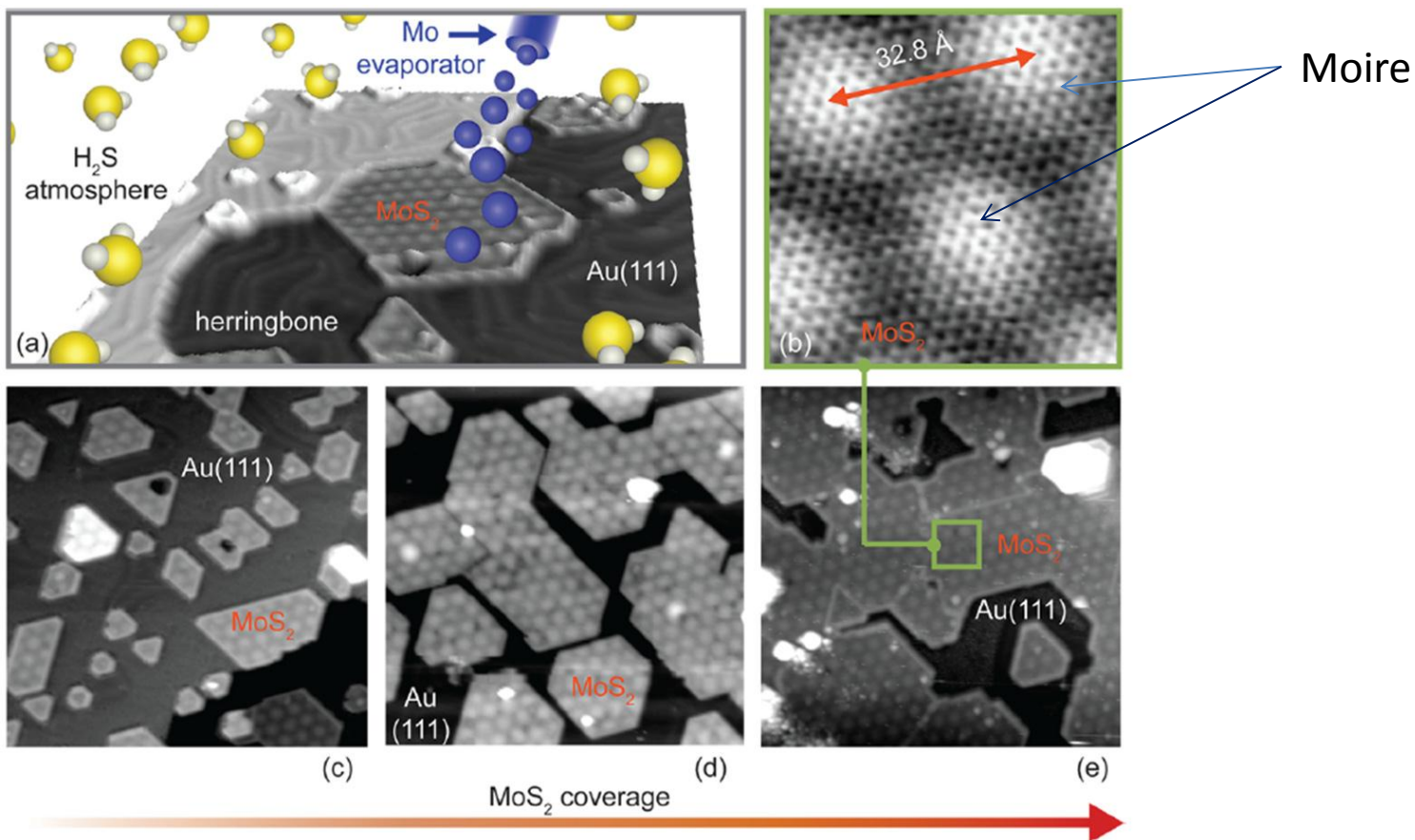


VPD using gas precursors

Mo + H₂S UHV-compatible

S.S. Grønborg, et al., *Langmuir*, **31**, 9700 (2015)

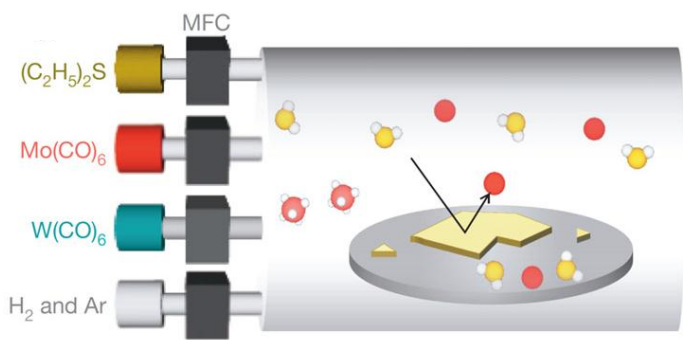
Typically submonolayer coverage \longrightarrow Cycle-based epitaxial growth



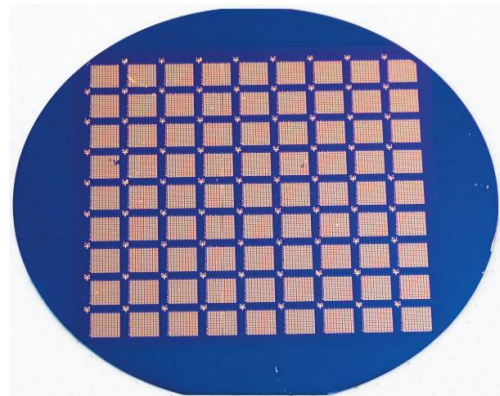
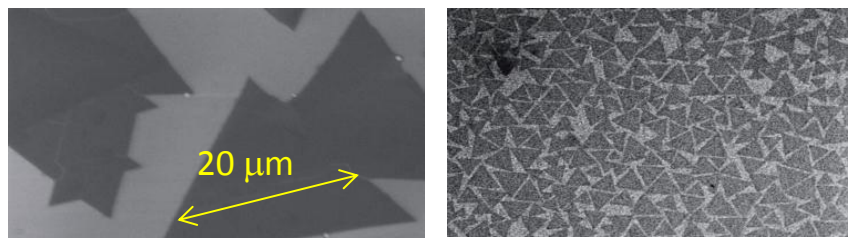
Wafer scale synthetic 2D Semis- MOCVD

K. Kang et al., *Nature* 520, 657 (2015)

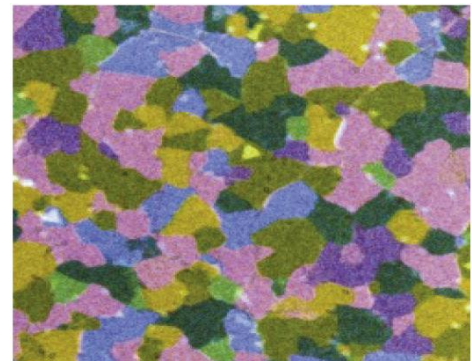
All-gas precursors



Full coverage, good thickness control,
High spatial homogeneity



MoS₂ (and WS₂)
on 4-inch Si/SiO₂



The material
is polycrystalline

FETs : 99% yield
Record field effect mobility (at that time)
for synthetic material ~ 30 cm²/Vs @RT

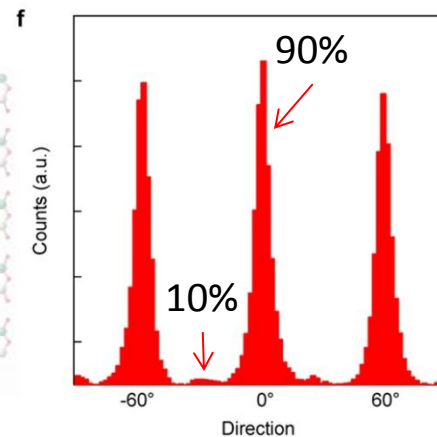
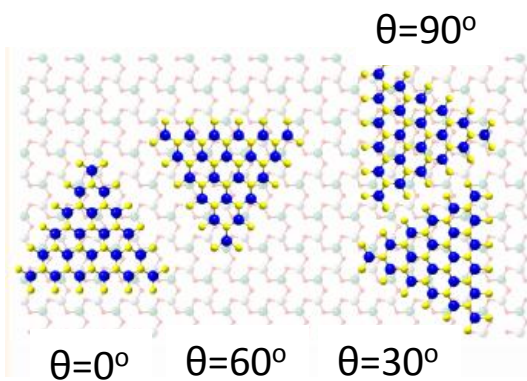
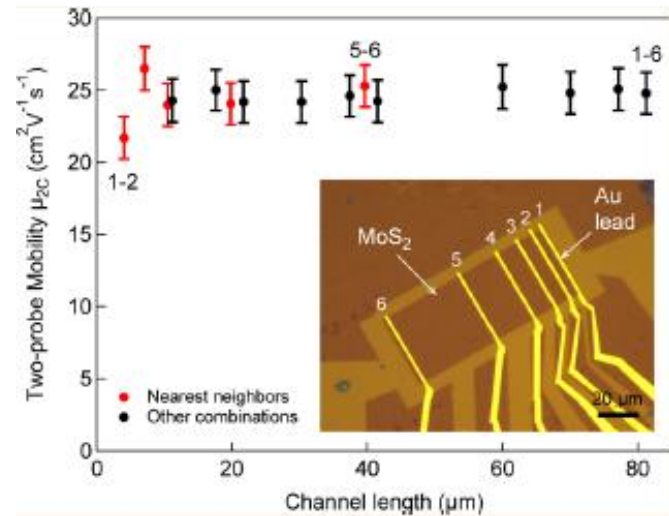
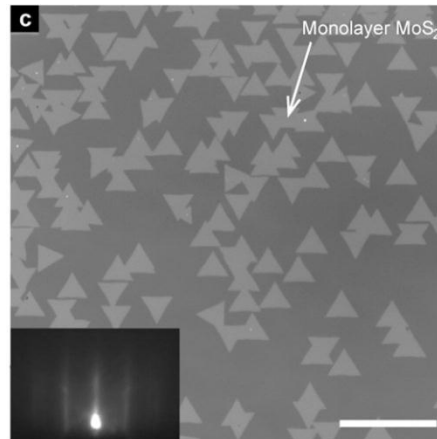
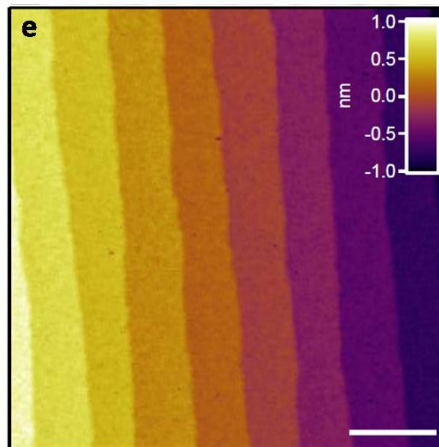
Need for epitaxial growth of
single crystalline 2D layers

Large area epitaxial MoS₂ on c-sapphire

D. Dumcenco et al., *ACS Nano* 9, 4611 (2015)

C-sapphire preparation

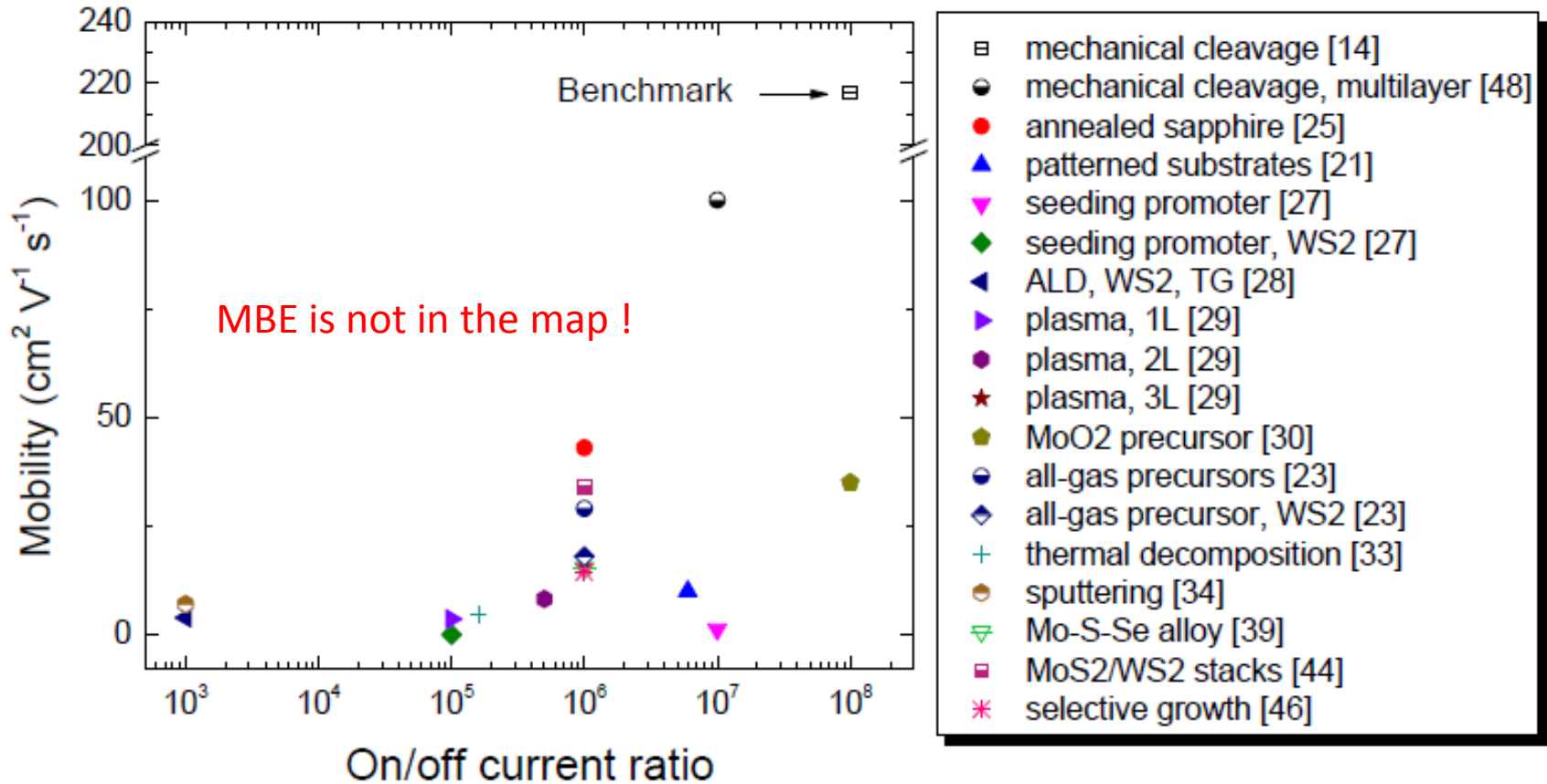
1000 C / 1 h in air



Mobility not affected much by grain boundaries

Summary of Results

Review: J. Li and M. Oestling, *Electronics* 4, 1033 (2015)



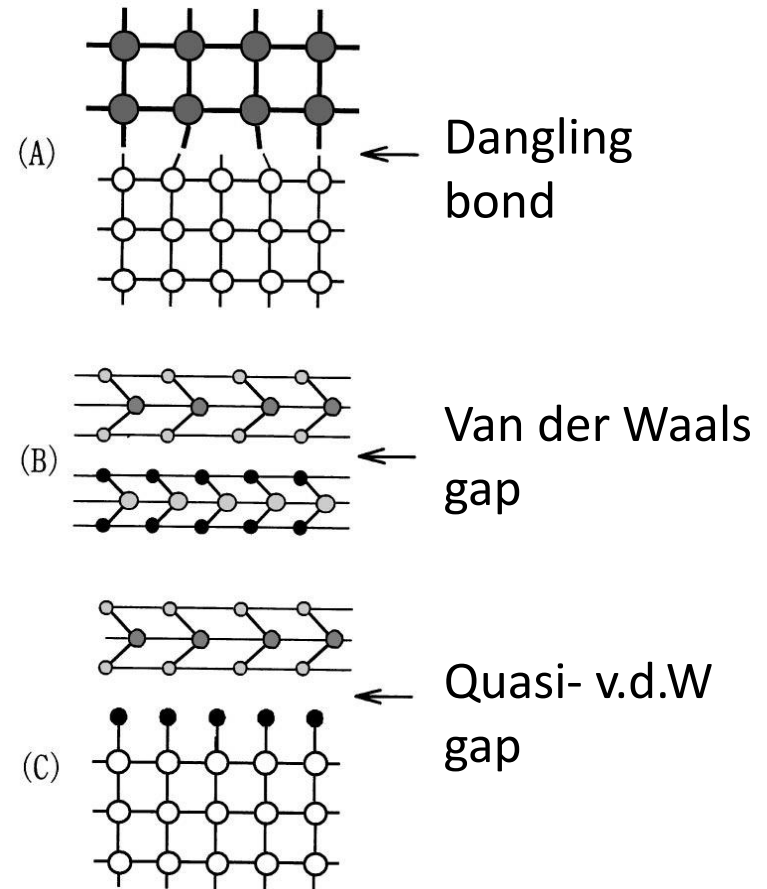
Van der Waals Epitaxy by MBE

Early pioneering work (80's and 90's)

A. Koma and co-workers, Japan

First to introduce the concept of v. d. Waals epitaxy by MBE

- No heteroepitaxial defects (e.g. misfit dislocations)
- No strain-own lattice constant
- In-plane alignment (rotationally commensurate)



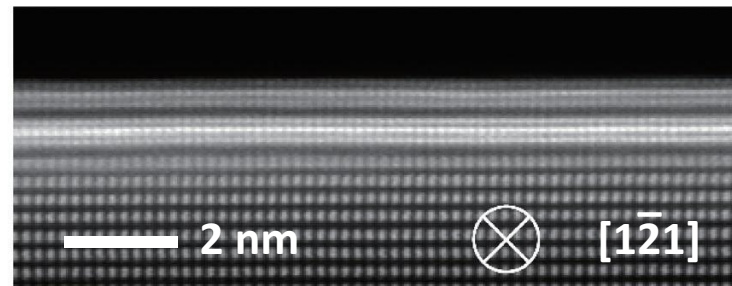
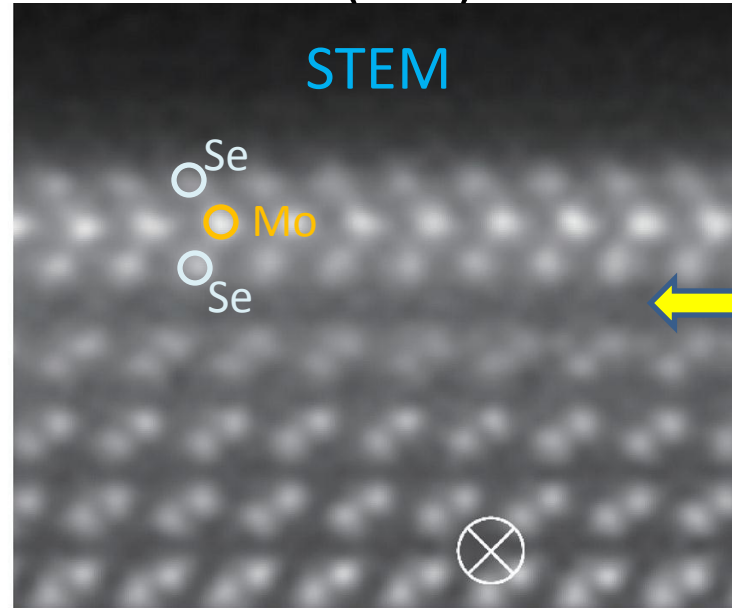
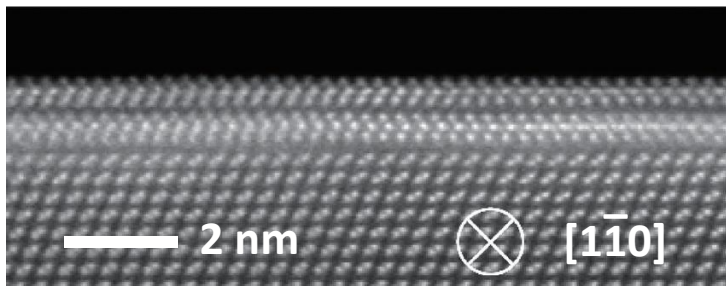
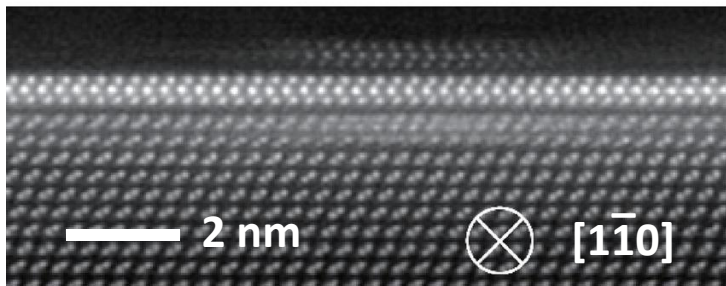
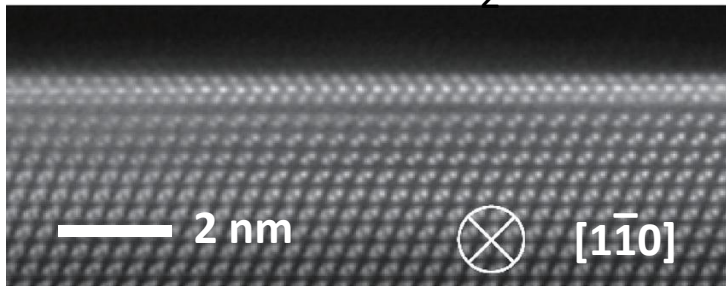
A. Koma, *J. Cryst. Growth* 201/202 236 (99)

R. Schlaf, W. Jaegerman and co-workers, *J. Appl. Phys.* **85**, 2732 (1999)

Recent example of v. d. Waals epitaxy

K. Onomitsu et al., *Applied Physics Express* **9**, 115501 (2016)

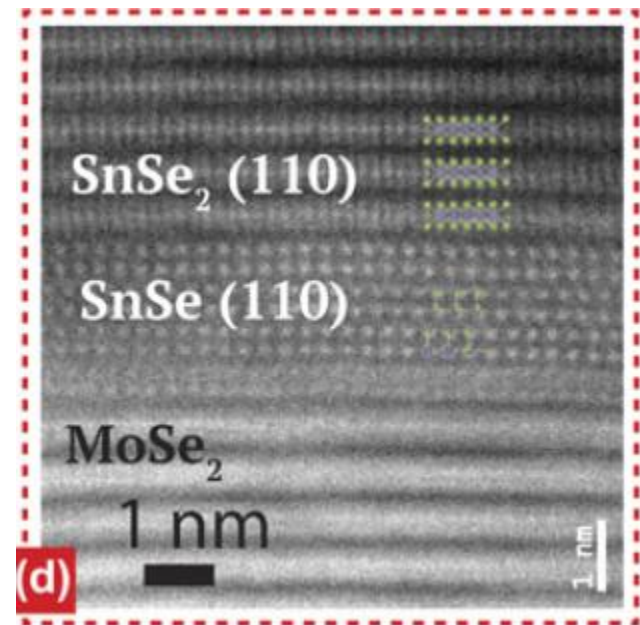
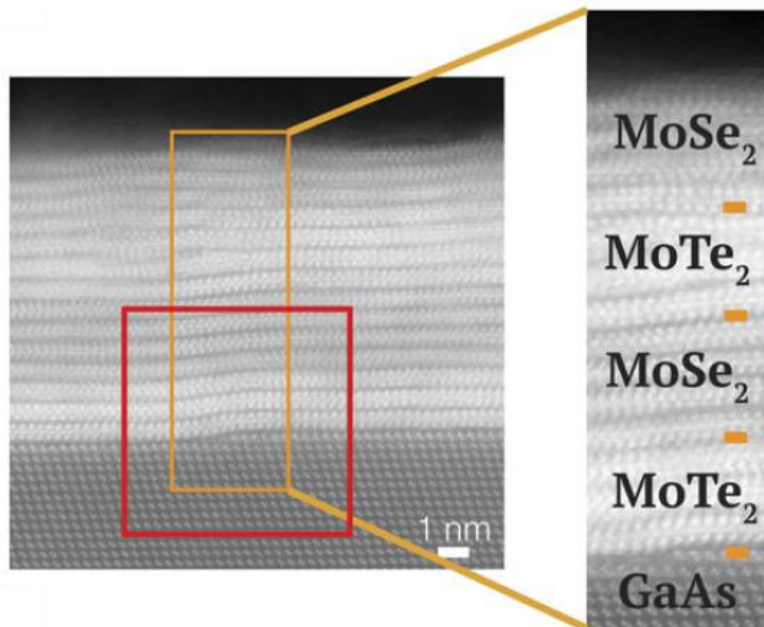
MoSe₂ on **Se-terminated** GaAs (111) B



Key point: Extremely slow Mo evaporation rate: 1 L/3300 sec !

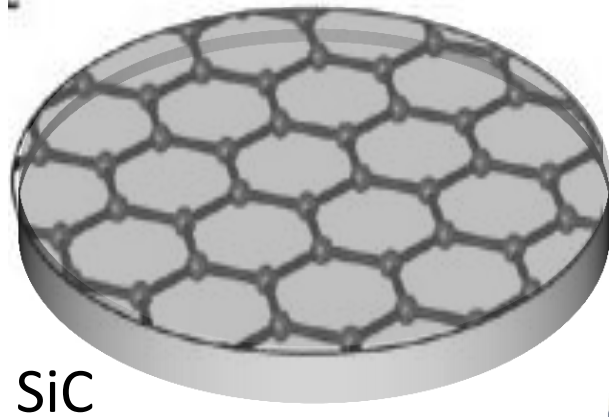
Controlled MBE growth of superlattices

S. Vishwanath, et al., *J. Mater. Res.* 31, 900 (2016)

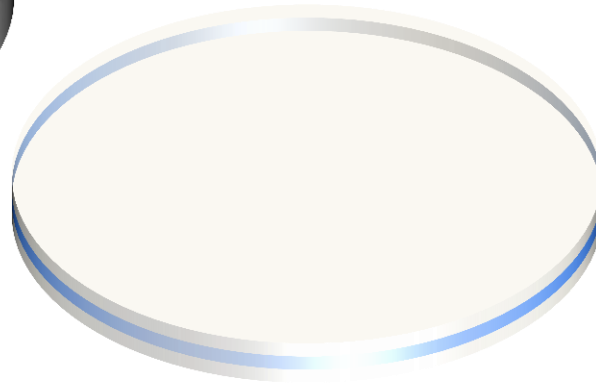


Crystalline substrate options

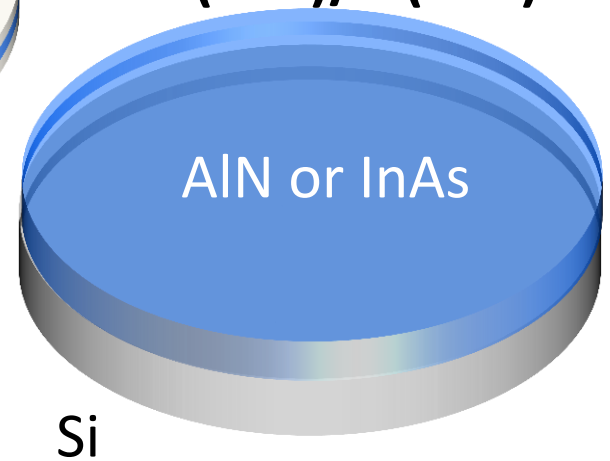
Epitaxial graphene on SiC



Sapphire(0001) C-plane



AlN(0001)/Si(111)
InAs(111)/Si(111)



- Expensive
- Small size (6 inch)
- Incompatible with advanced semiconductor processing in pilot lines

MoSe₂ on AlN-MBE growth details

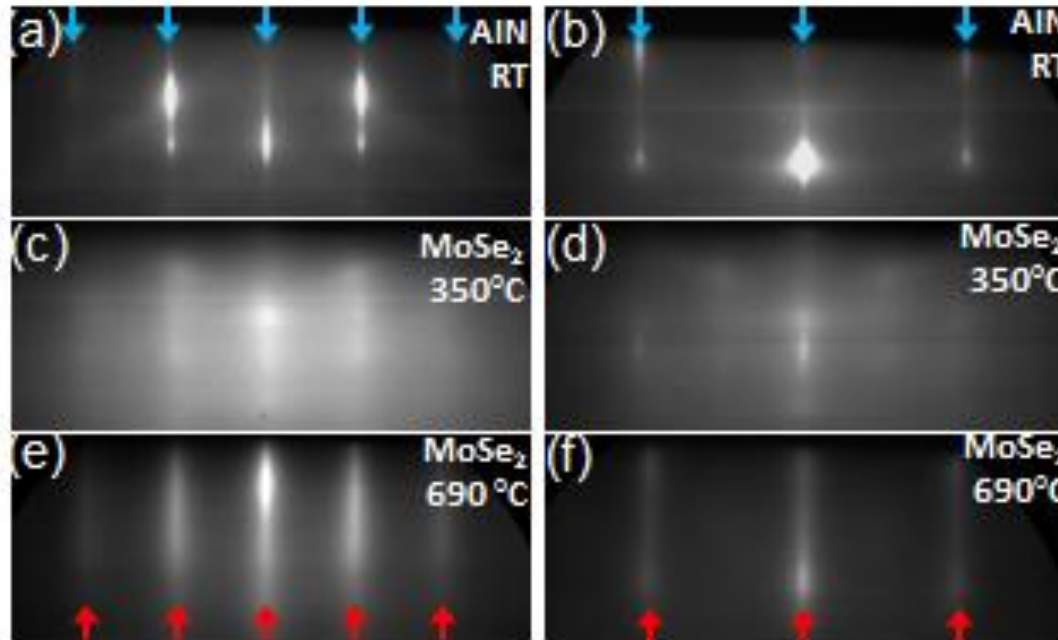
E. Xenogiannopoulou et al., *Nanoscale* 7, 7896(15)

MOCVD AlN(0001)/Si(111) provided by IMEC

Preparation: HF-based solution + UHV annealing @ 700 C, 10 min

[11-20] azimuth

[1-100] azimuth



Two-step growth

Mo : e-gun

Se : effusion cell

Se/Mo : 15/1

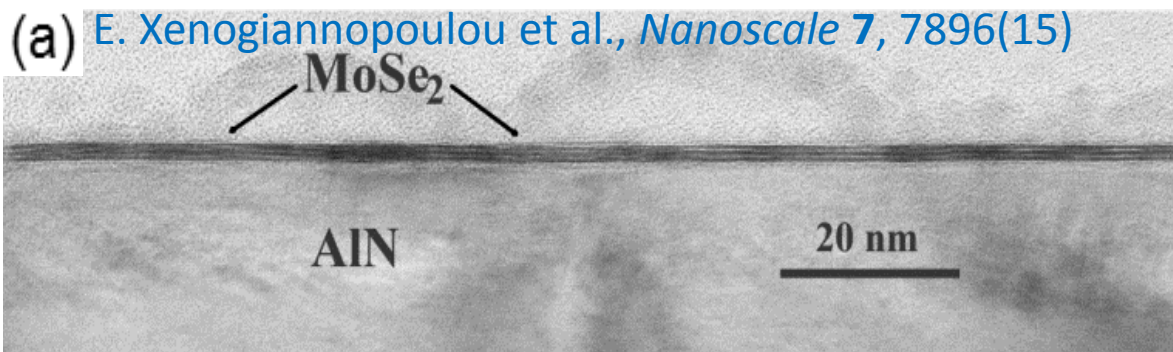
Rate : 1 ML /30 sec

UHV PDA, 10 min

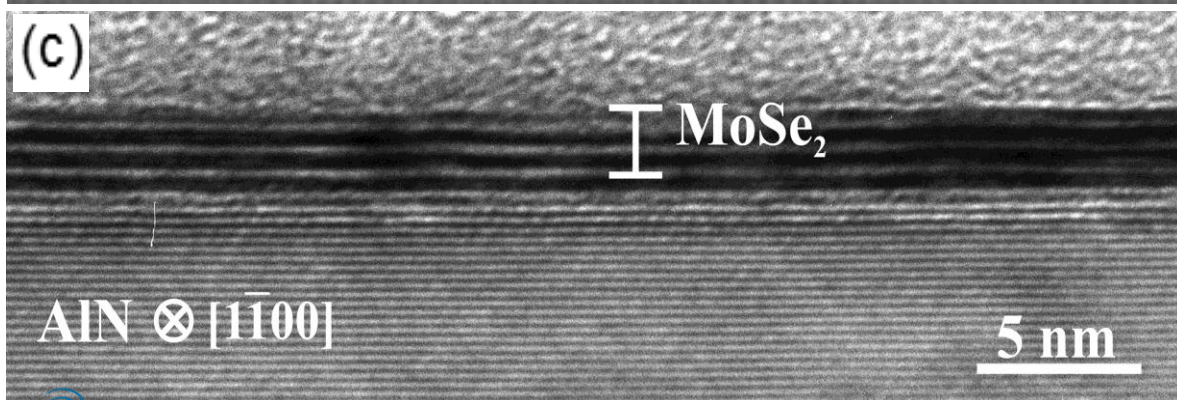
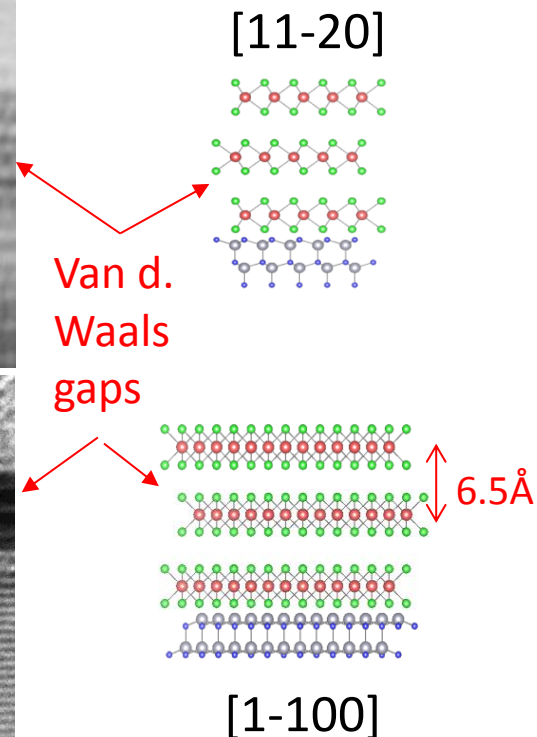
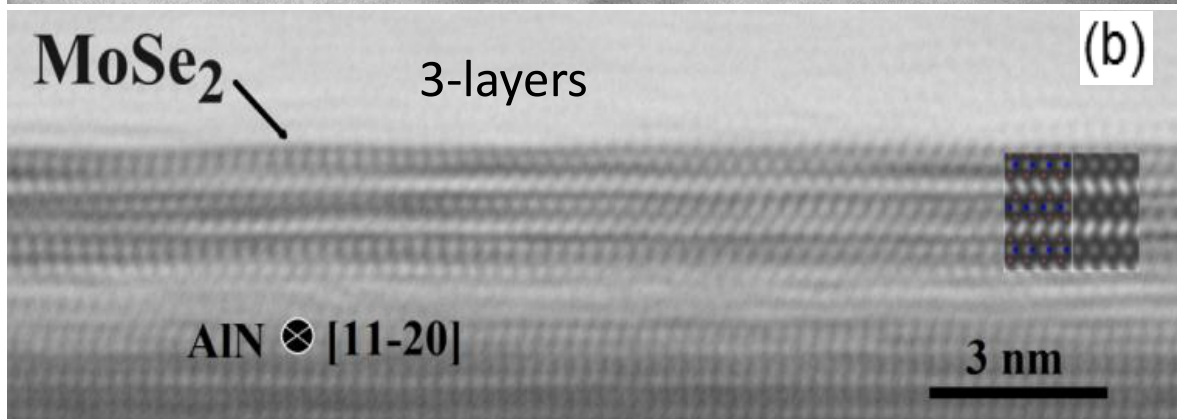
To improve crystallinity

- Good in-plane alignment no 30° or 90° rotation domains
- Epitaxial despite +6% lattice mismatch

MoSe₂ on AlN -HRTEM



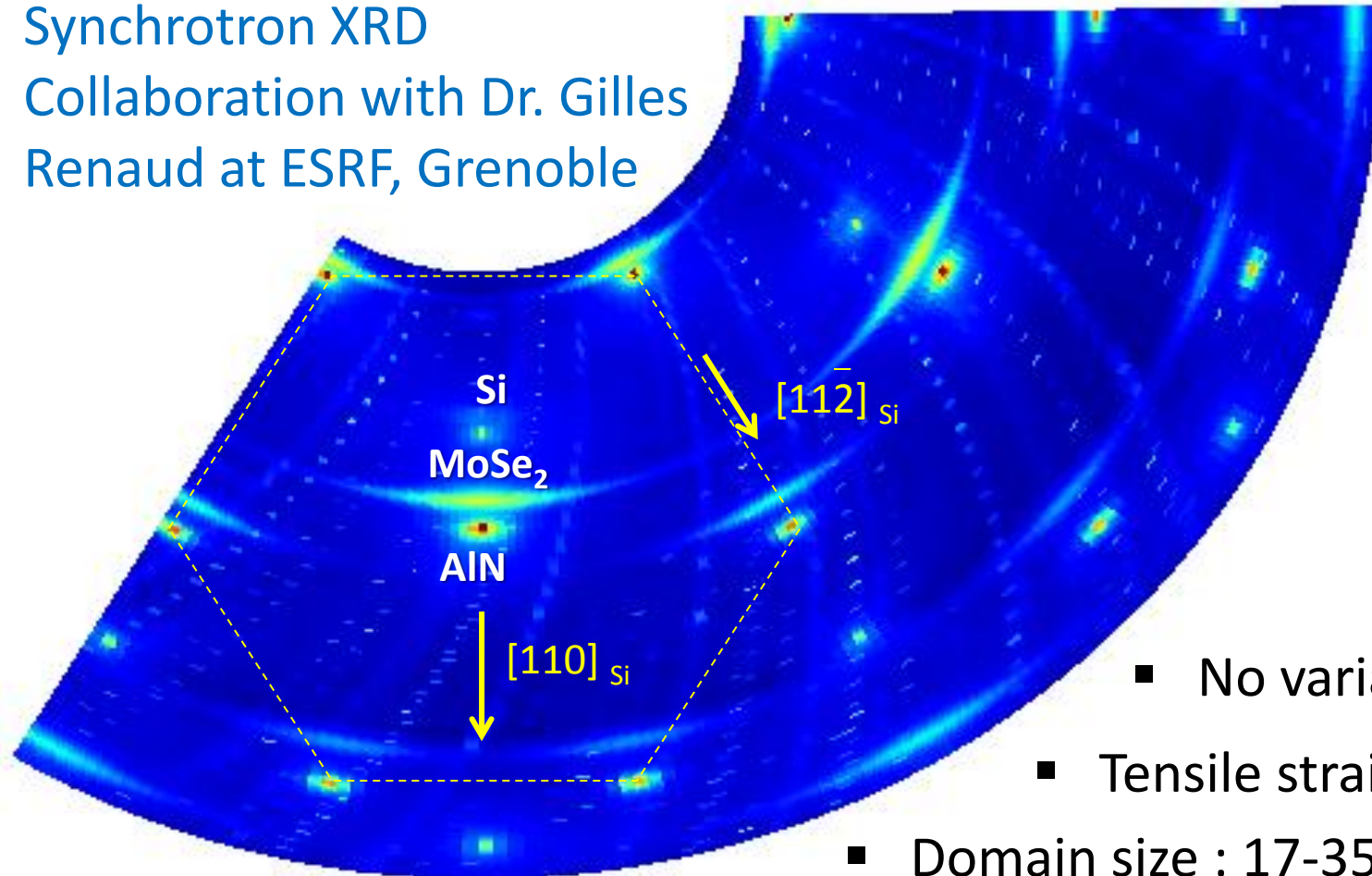
Continuous, uniform, smooth surface and interfaces
No dislocations or defects



MoSe₂ Reciprocal Space Mapping

Synchrotron XRD

Collaboration with Dr. Gilles Renaud at ESRF, Grenoble



- No variation with T_{ann}
- Tensile strain $\sim 0.3\%$
- Domain size : 17-35 nm
- Preferential orientation but large in-plane mosaic spread of MoSe₂ (7° FWHM) compared to AlN.

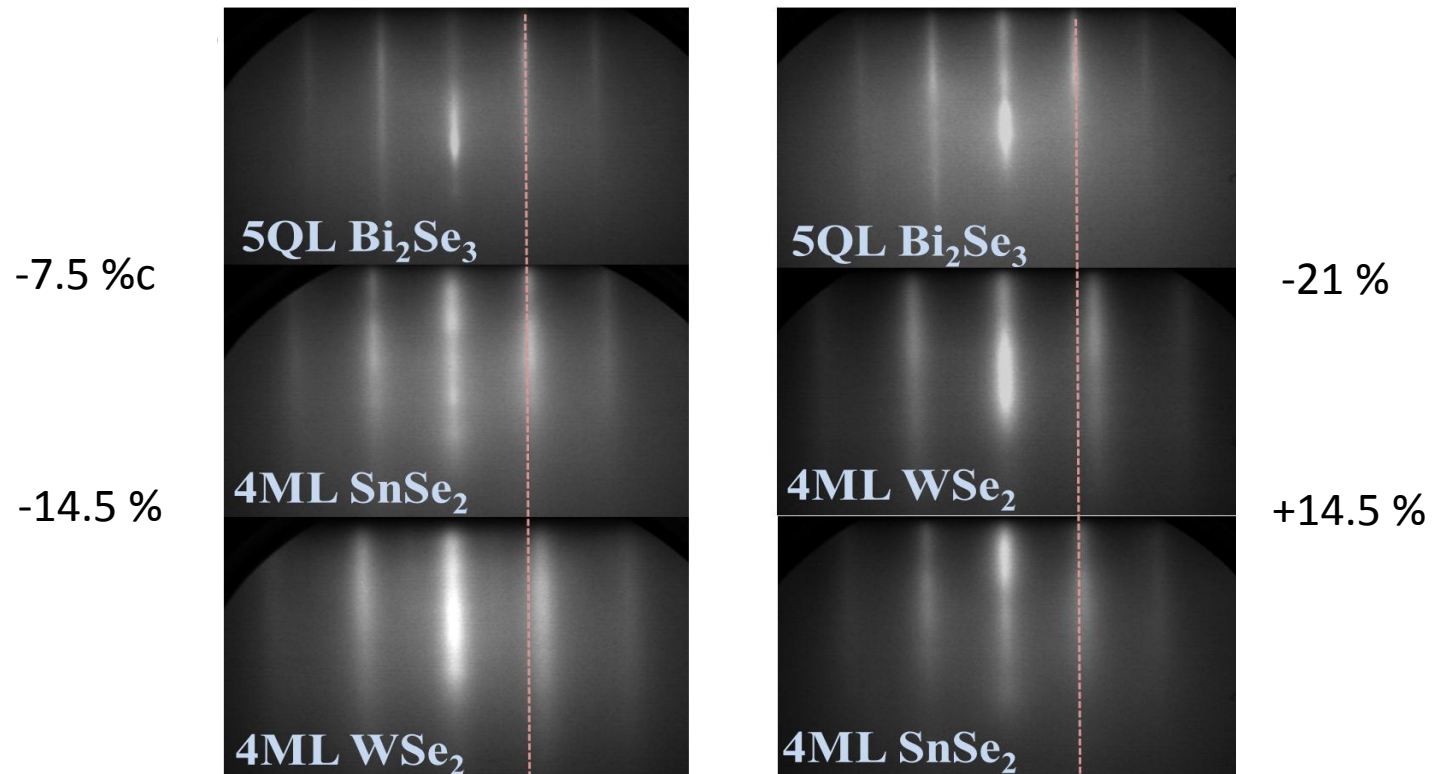
SnSe₂ / WSe₂ vdW on Bi₂Se₃ / AlN

Bi₂Se₃ is grown with good quality on AlN P. Tsipas et al., *ACS Nano* 8, 6614 (2014)

Improves the quality of SnSe₂ and WSe₂ epilayers

Growth is limited to < 300 C, since Bi₂Se₃ is unstable above this T

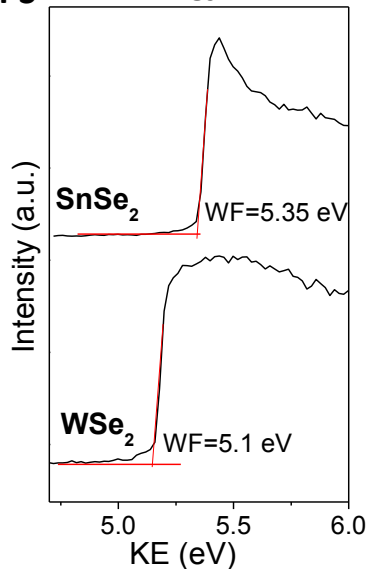
RHEED [11-20] azimuth



Azimuthal alignment despite large lattice mismatch → vdW epitaxy

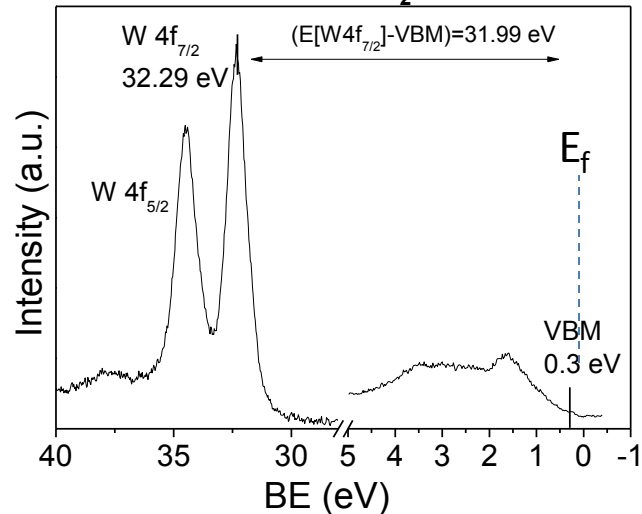
WSe₂/SnSe₂ v.d. Waals Heterostructures

UPS Low-energy cutoff

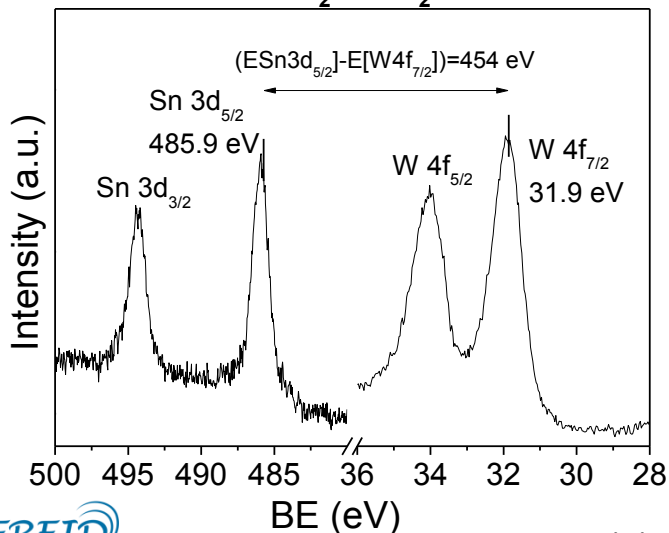


Kraut's method
for band offsets

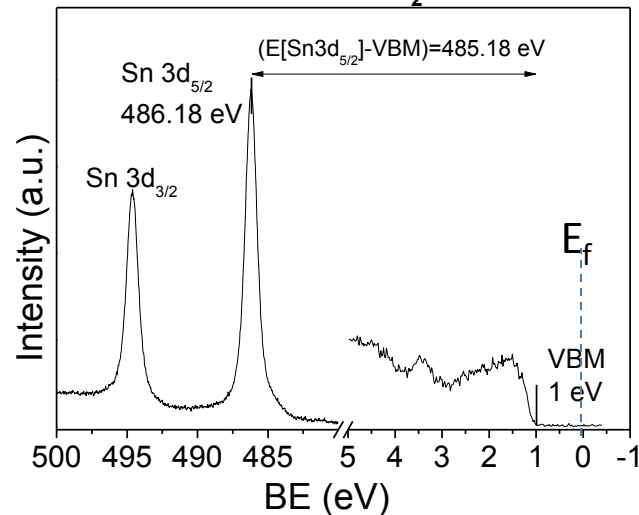
XPS Thick WSe₂



XPS SnSe₂/WSe₂

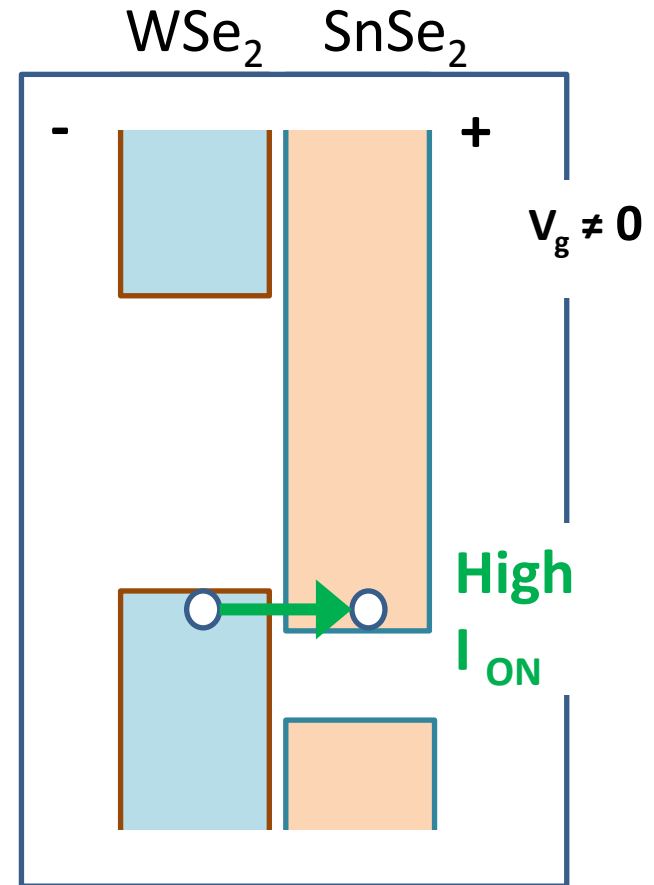
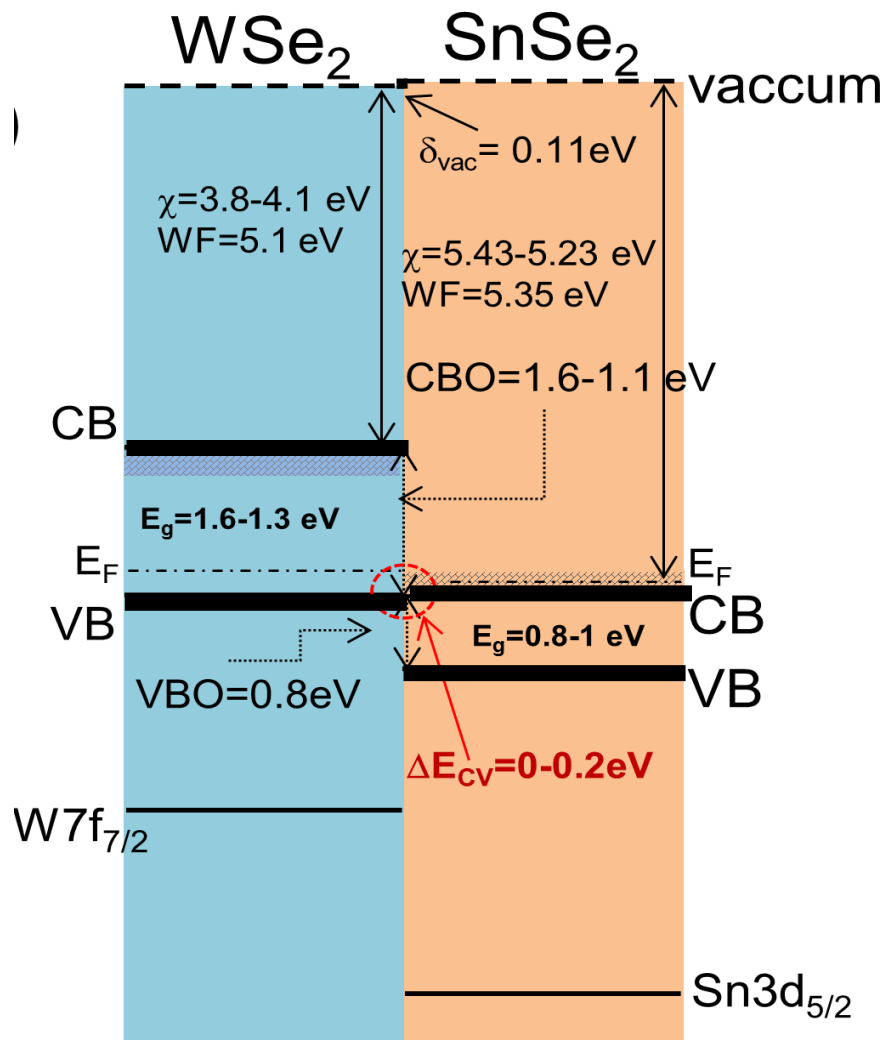


XPS Thick SnSe₂



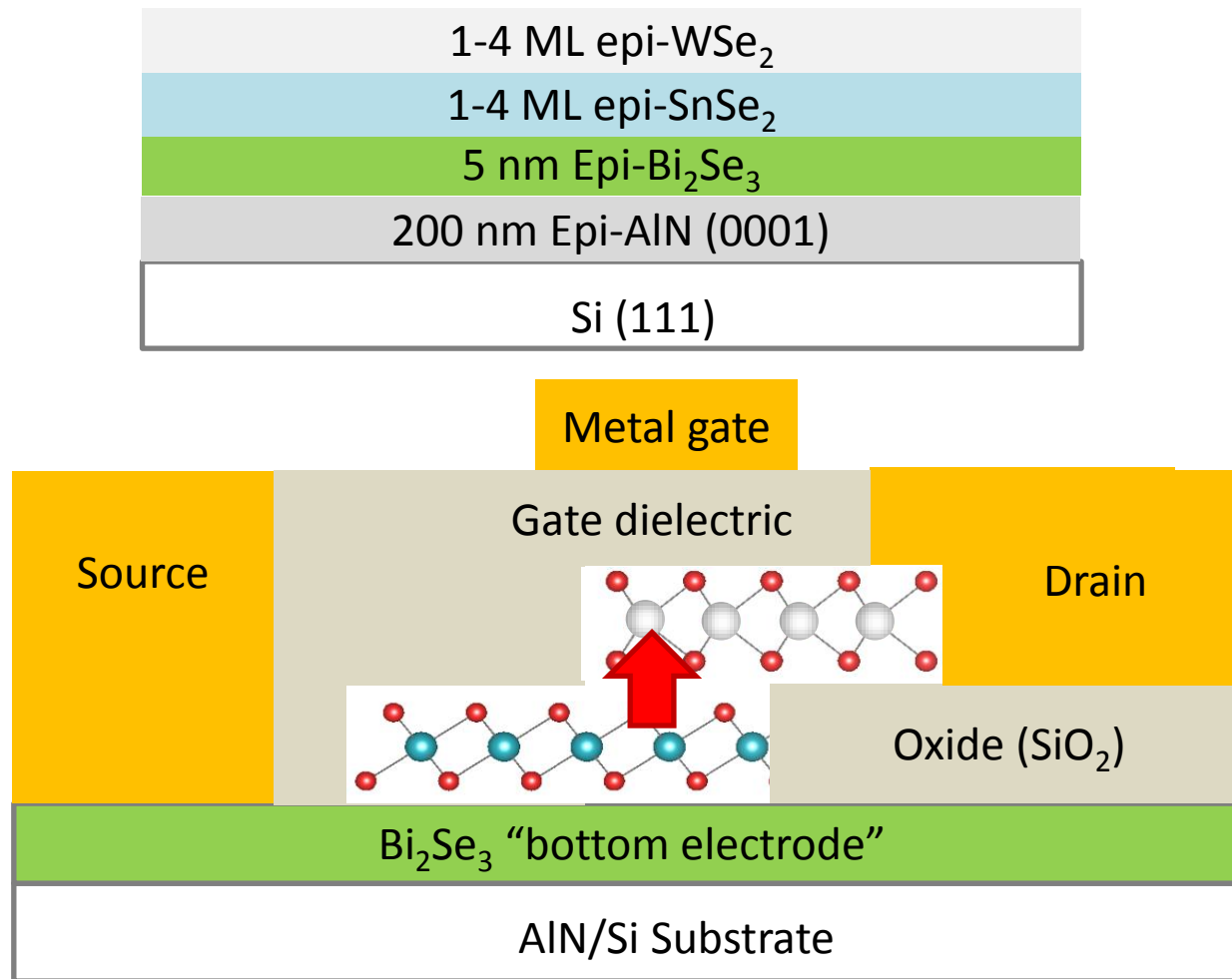
Ideal band line-up for 2D TFETs

K.E. Aretouli et al., *ACS Appl. Mater Interfaces* **8**, 23222 (2016)



Nearly broken gap
high performance 2D TFETs

2D Vertical TFET Device layout



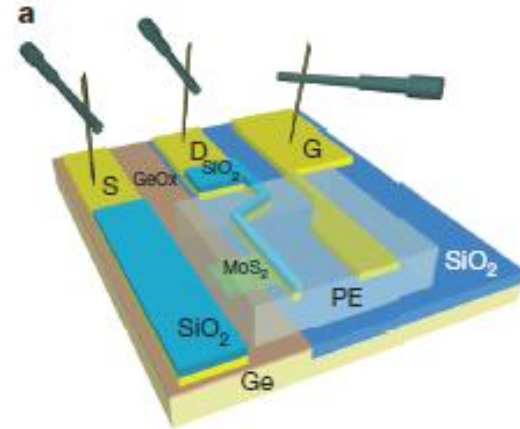
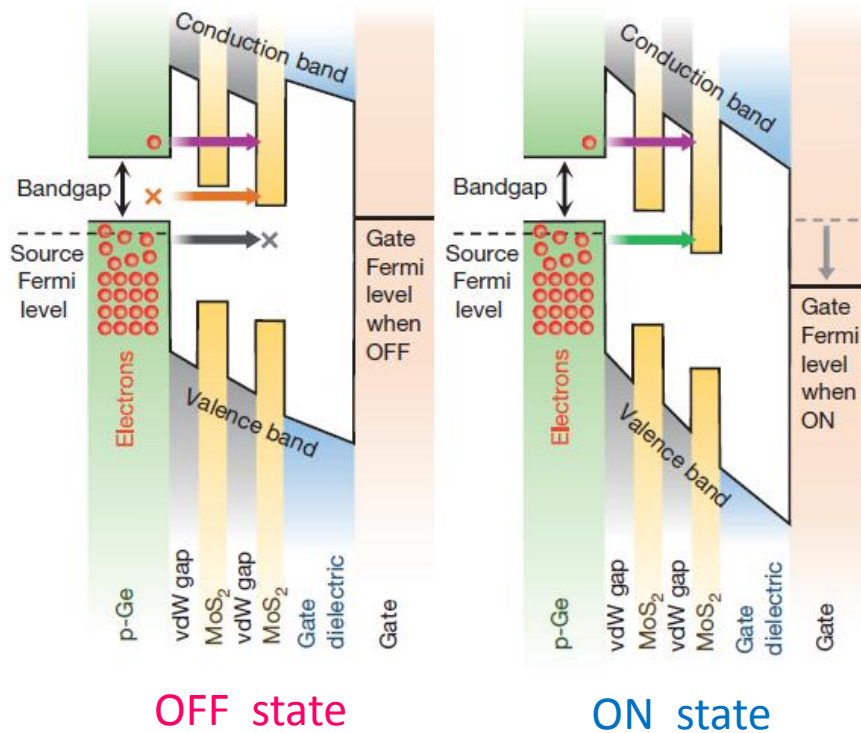
Recently implemented using WSe₂/SnSe₂ flakes and back-gating, but $SS > 100$ mV/dec [T. Roy et al., *Appl. Phys. Lett.* **108**, 083111 \(2016\)](#)

Hybrid 3D -2D vertical TFETs

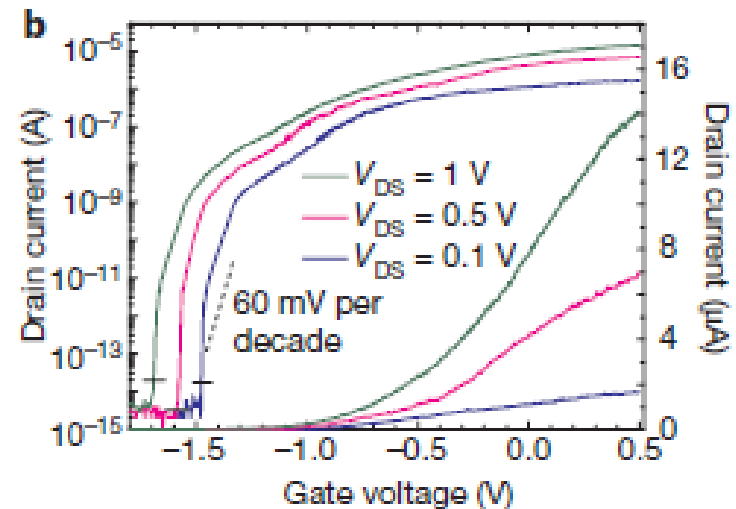
D. Sarkar, G. Banerjee and co-workers, *Nature* 526, 91 (2015)

Important new development

3D (Ge) / 2D (MoS₂) TFET
on exfoliated flakes



$SS < 60 \text{ mV/dec}$, $V_{ds} = 0.1 \text{ V}$

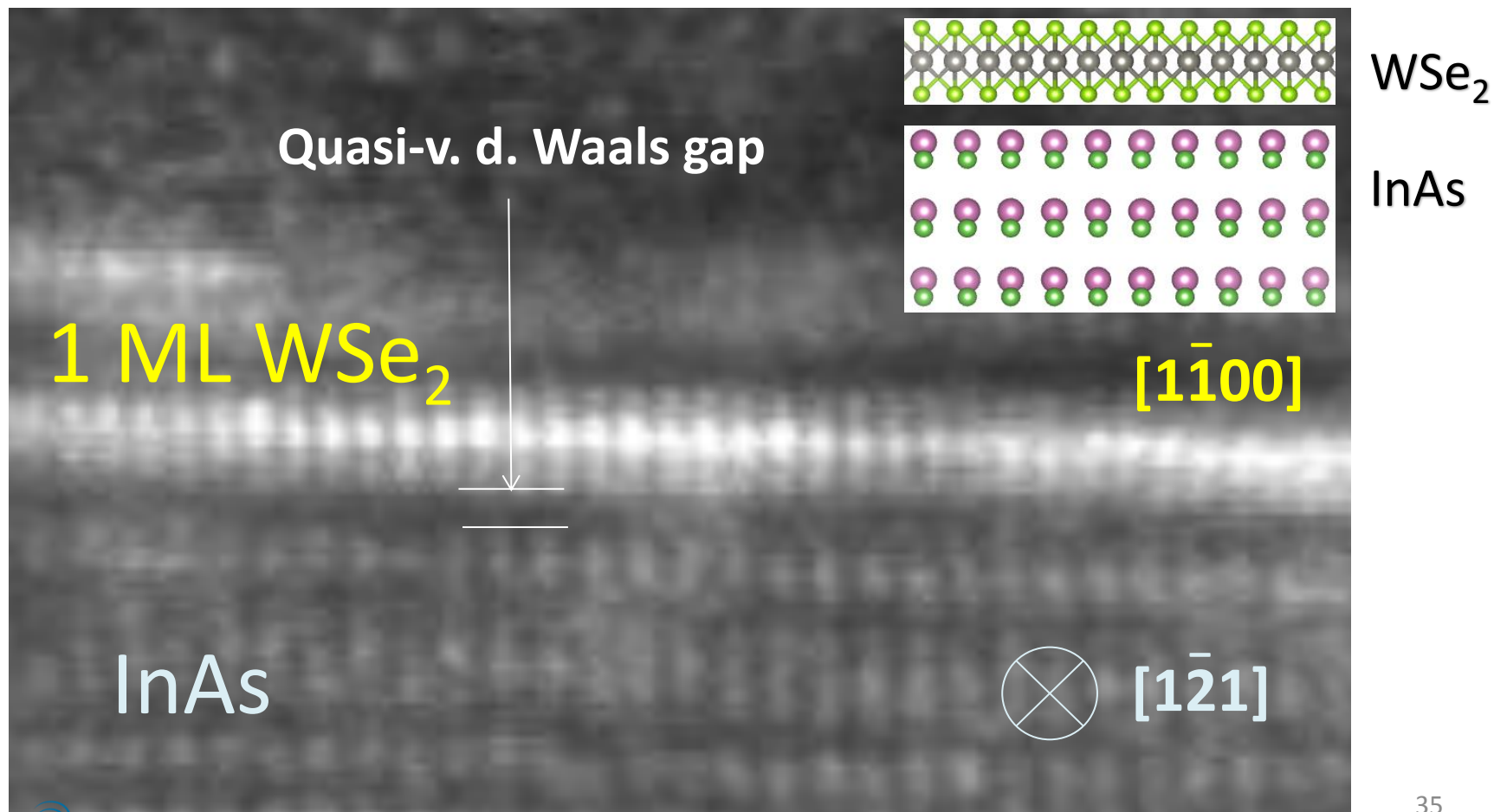


Epitaxy of WSe_2 on $\text{InAs}/\text{Si}(111)$

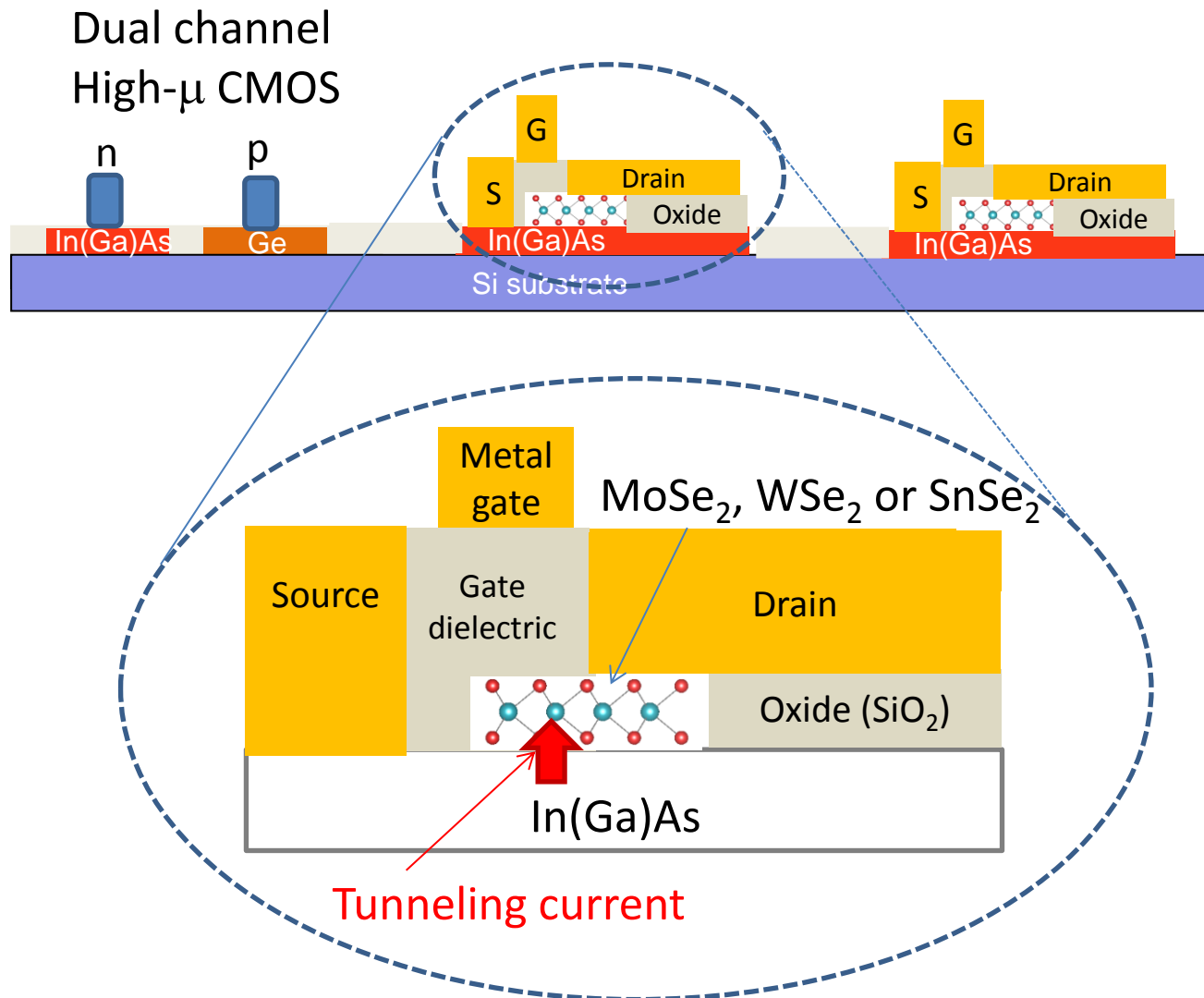
Cross section STEM

NCSR-D-CEA-CNRS/LTM collaboration (unpublished)

(C. Alvarez, H. Okuno, T. Baron, A. Dimoulas)

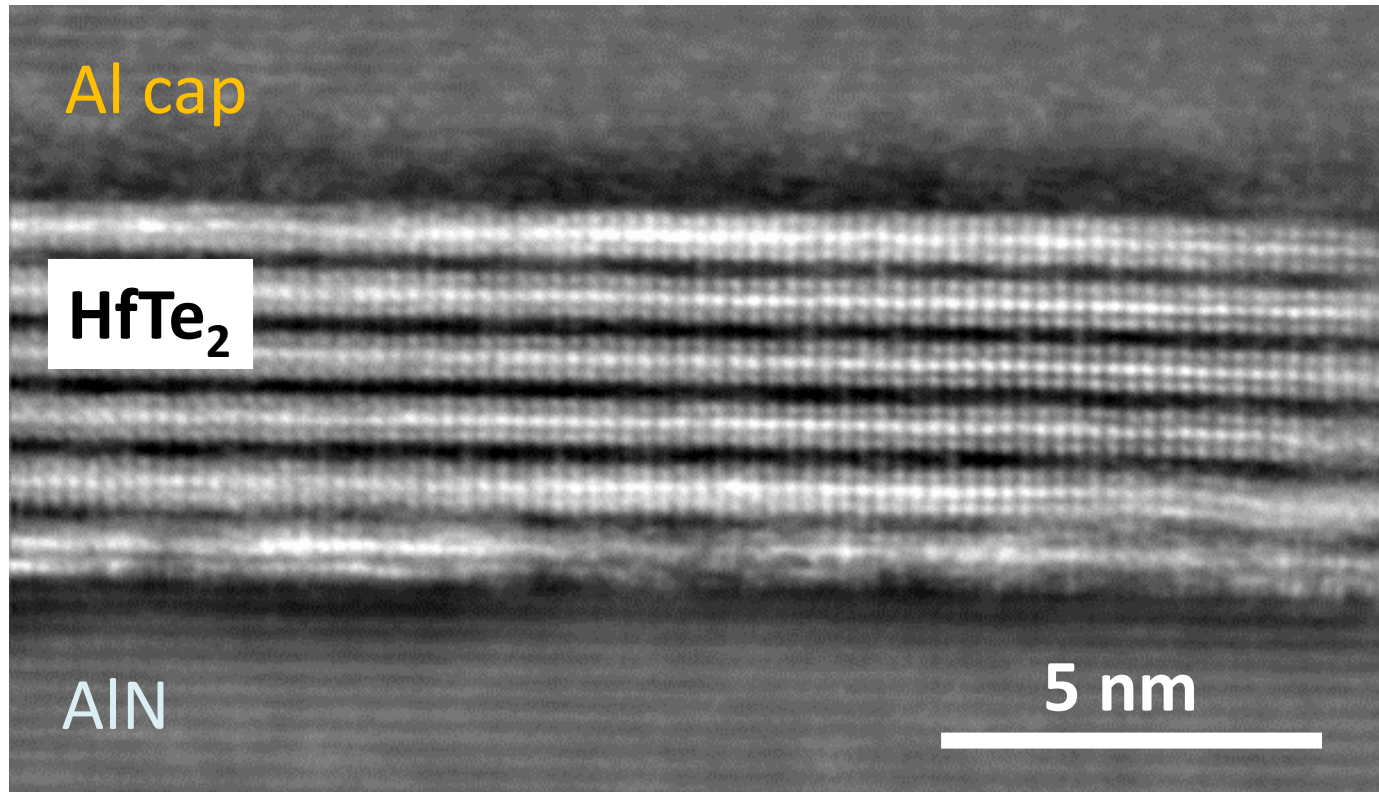


Co-integration of 3D-2D vertical TFET with advanced dual channel CMOS platform

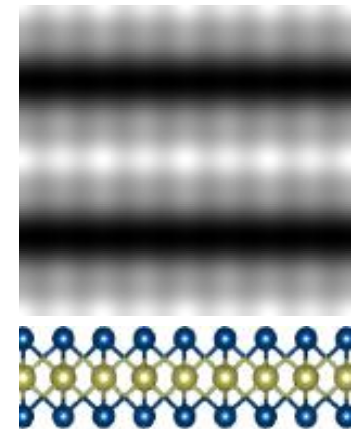


HfTe₂ on AlN – HAADF STEM

NCSR-D-CEA collaboration
C Alvarez, H. Okuno, A. Dimoulas

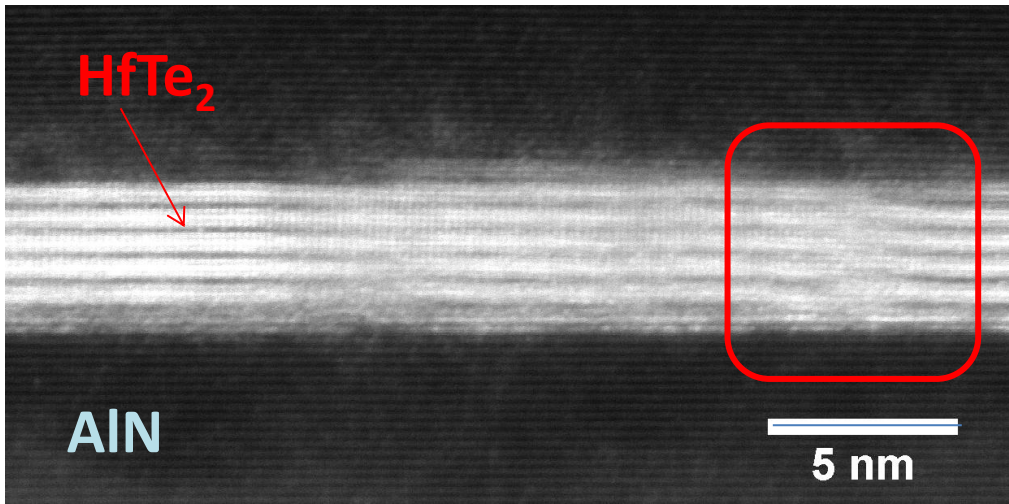


simulation
(1-100) zone



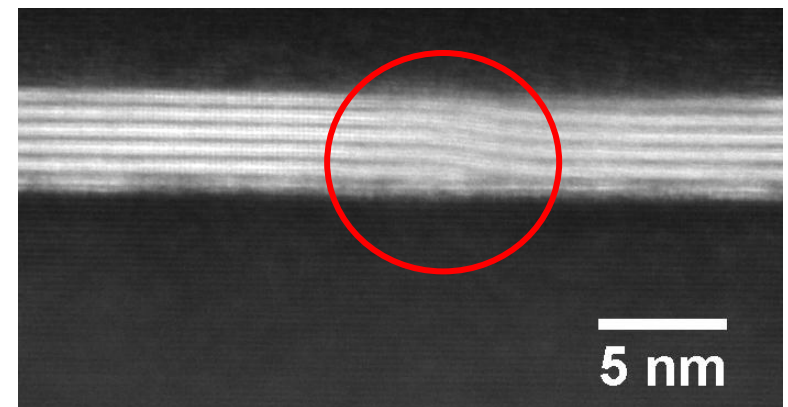
Consistent
with 1T-HfTe₂

HfTe₂ Film Defects and Faults



- Layer overlapping
- Shows competing grains during growth

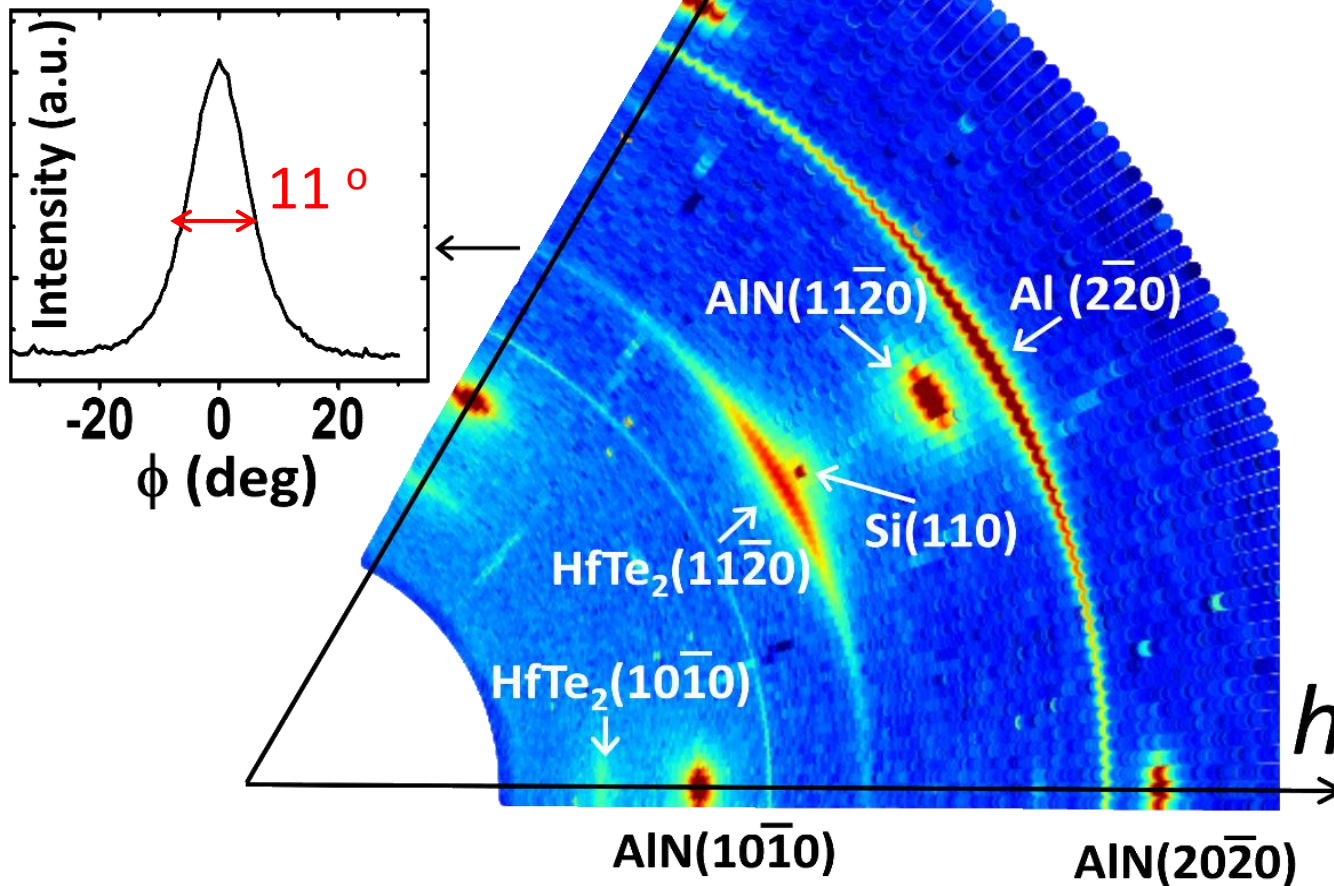
- Grain boundary causing misalignment of the layers
- No clear correlation of defects with the substrate



HfTe₂ Reciprocal Space Mapping

S. A. Giamini et al., *2D Mater.* 4, 015001 (2017)

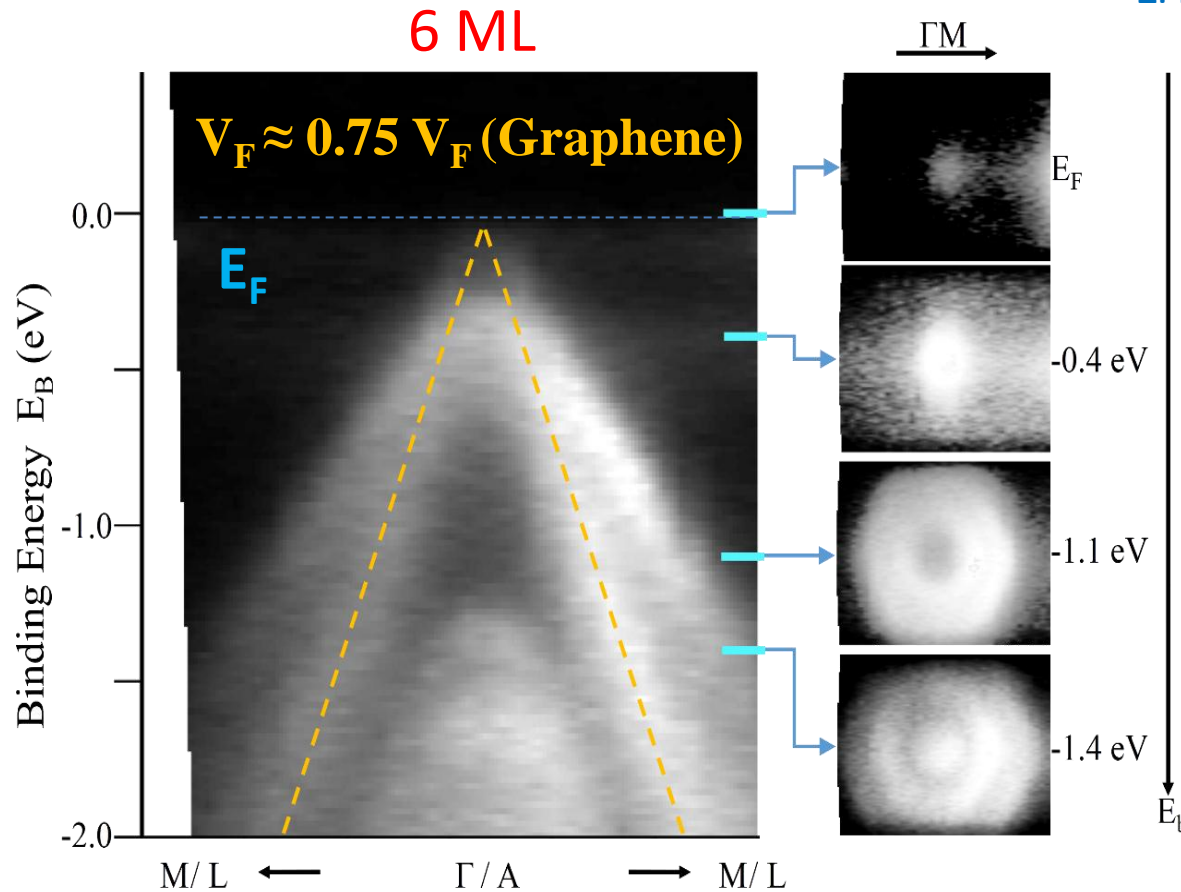
Synchrotron HRXRD
at ESRF



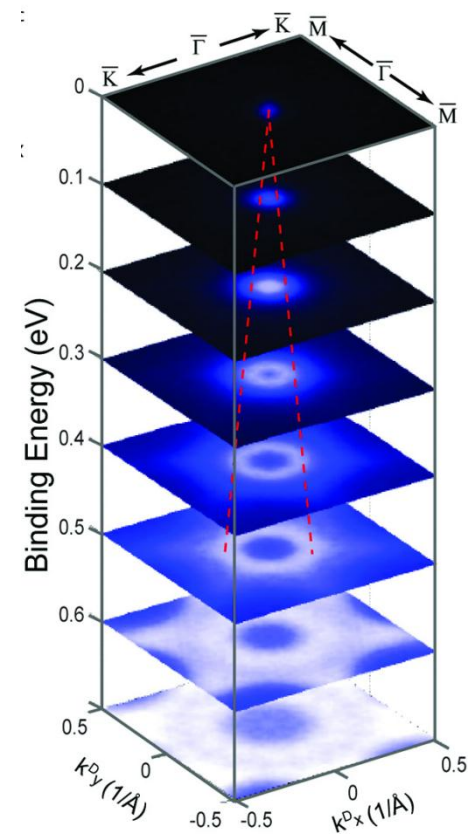
MBE HfTe₂ in-situ ARPES “Dirac-like” cone

S. A. Giamini et al., *2D Mater.* **4**, 015001 (2017)

Z. K. Liu et al., *Science*, **343**, 864 (14)



Na₃Bi topological
3D Dirac semimetal

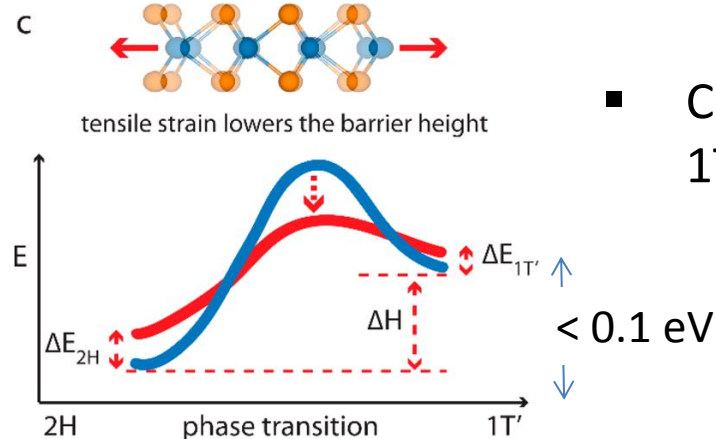
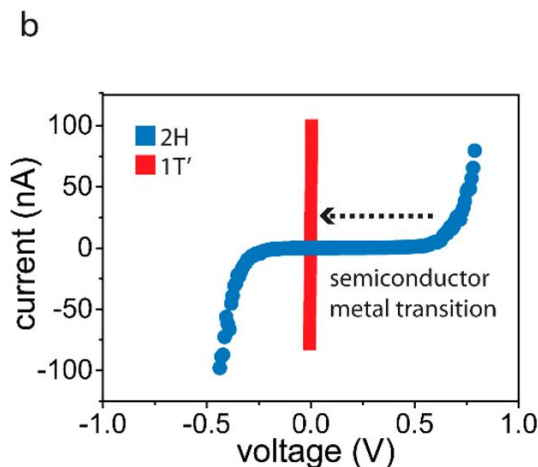
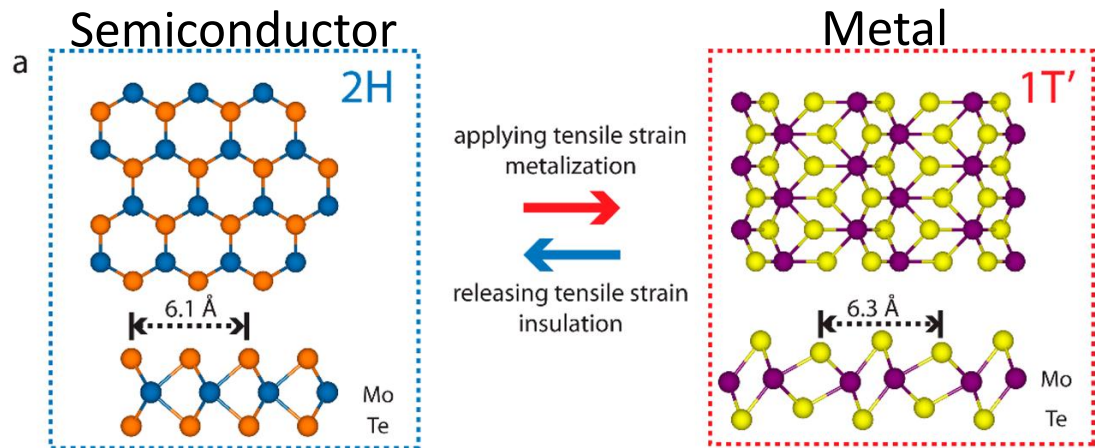


HfTe₂ an epitaxial topological Dirac semimetal?

Distorted 1T' MoTe₂

S. Song et al., *Nano Lett.* **16**, 188 (2016)

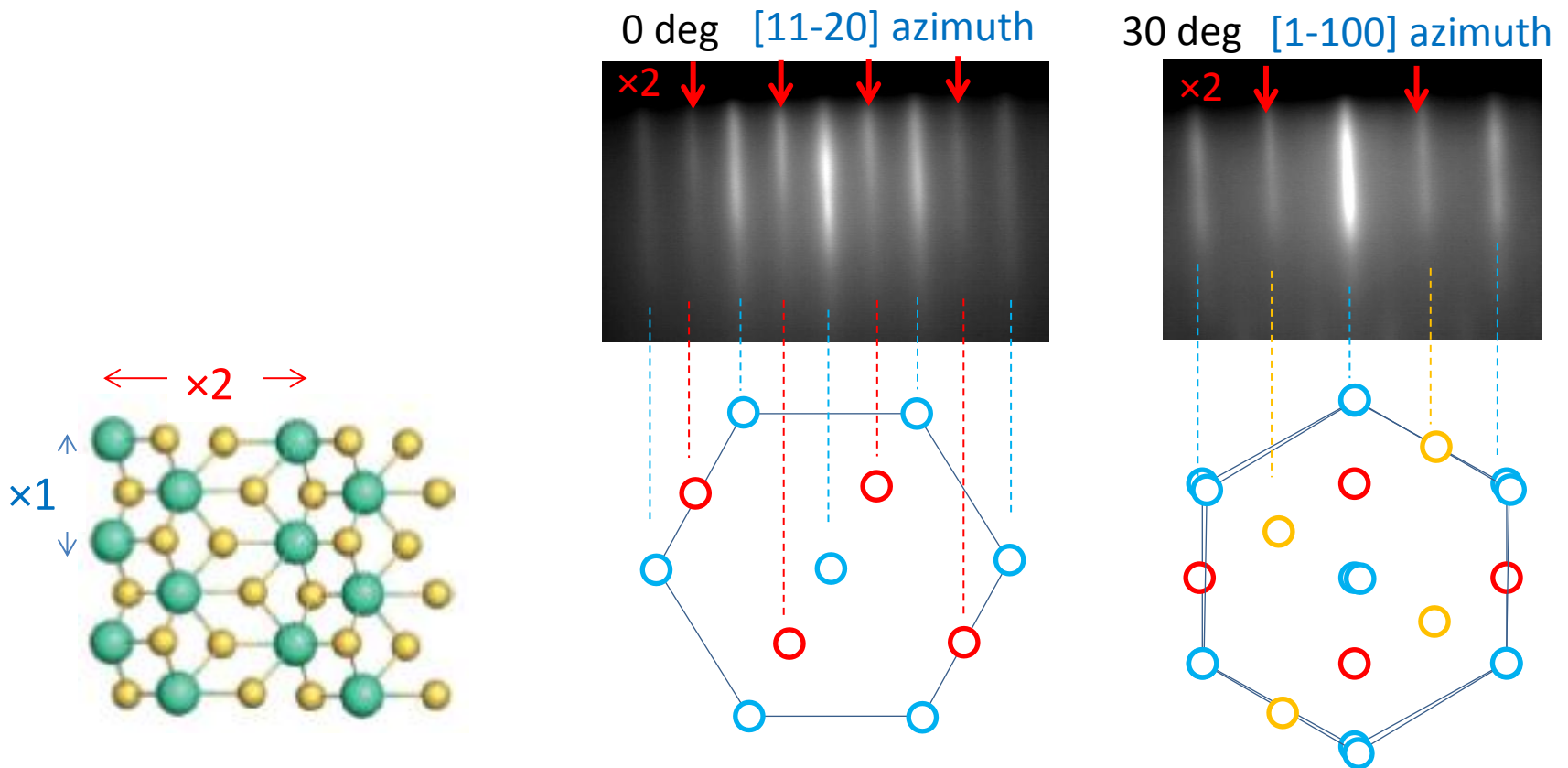
Strain-induced reversible semiconductor to metal transition (SMT)



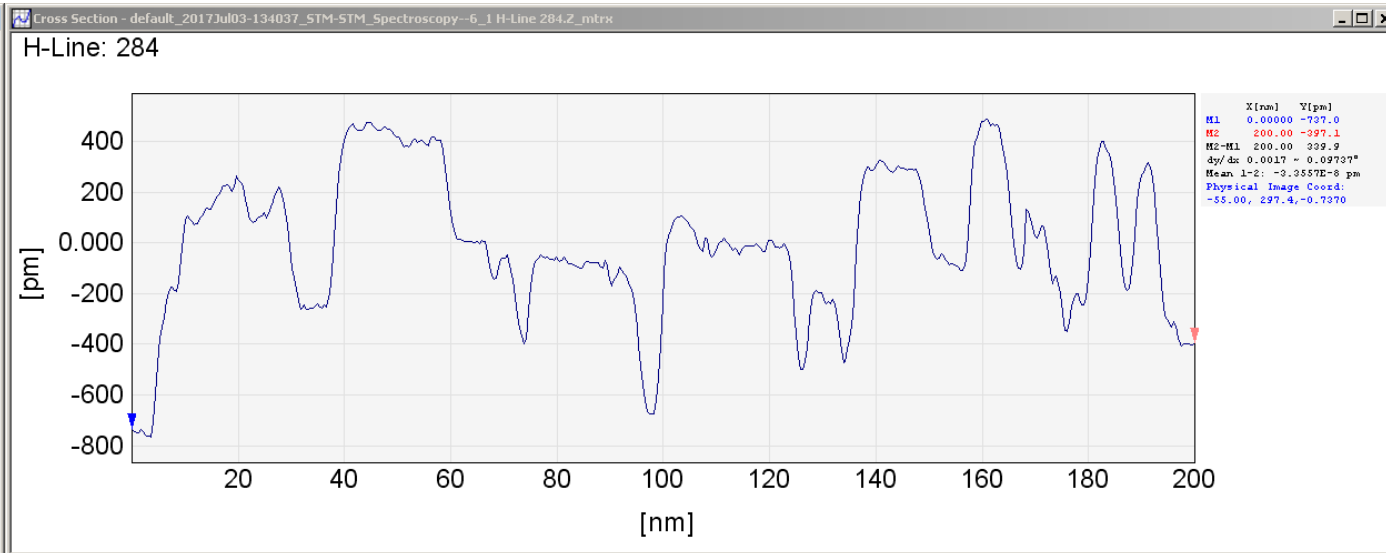
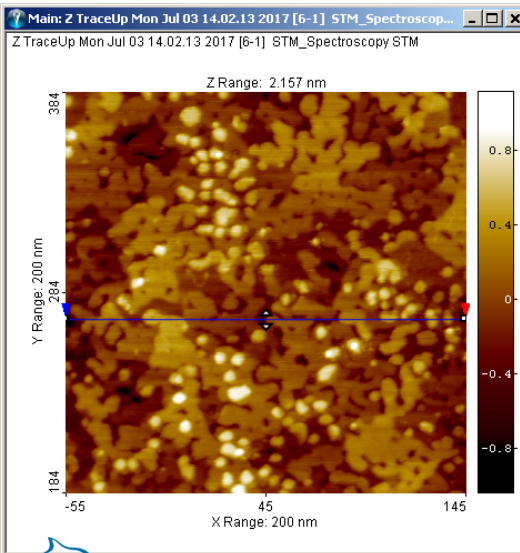
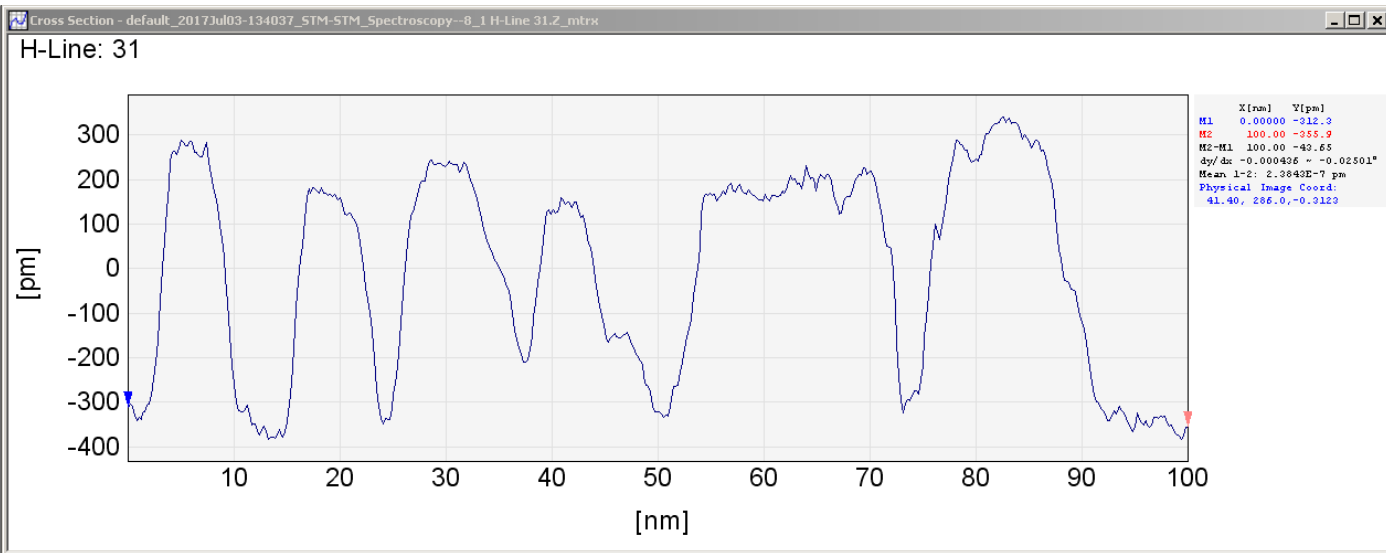
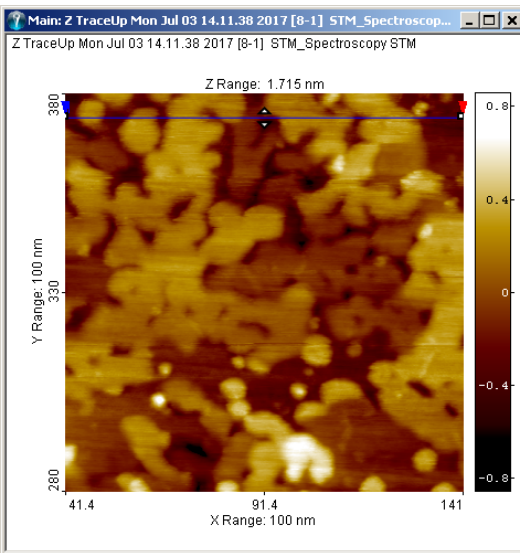
- Non volatile phase change memories
- (Steep slope ?) switching
- Can SMT be induced by field effect ?
- Can you make 1T' MoTe₂ epitaxial ?

MBE growth of MoTe_2 on $\text{InAs}(111)/\text{Si}(111)$

- Interrupted growth at 290 C + Annealing at 400 C , 1h
- 2x2 reconstruction: signature of a distorted T' phase



1-2 ML epitaxial MoTe₂ –*in situ* STM



Summary

- 2D materials can impact nanoelectronics (scaling, TFET)
- MBE 2D TMDs on AlN (0001) substrates:
Uniform growth /no heteroepitaxy defects/Large mosaicity
- MBE SnSe₂/WSe₂ vdW heterostructures
Ideal nearly broken gap band alignments for 2D TFETs
- MBE TMDs on InAs (111): Much improved epitaxy
- MBE HfTe₂ and ZrTe₂ : Dirac like cones at the zone center.
Candidates for epitaxial Dirac semimetals
- MBE Distorted 1T' MoTe₂ can be grown on InAs; stable at RT