

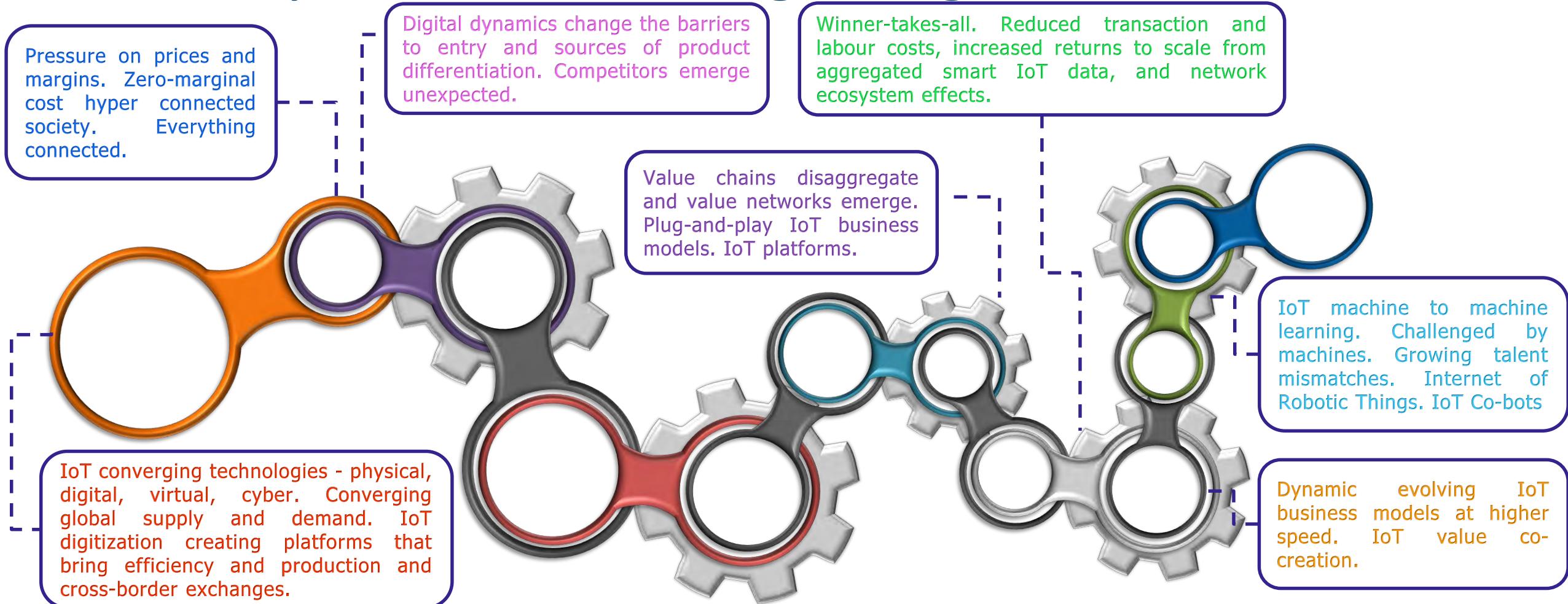
IoT Technology and Applications

Transformation across Industry Value Chain

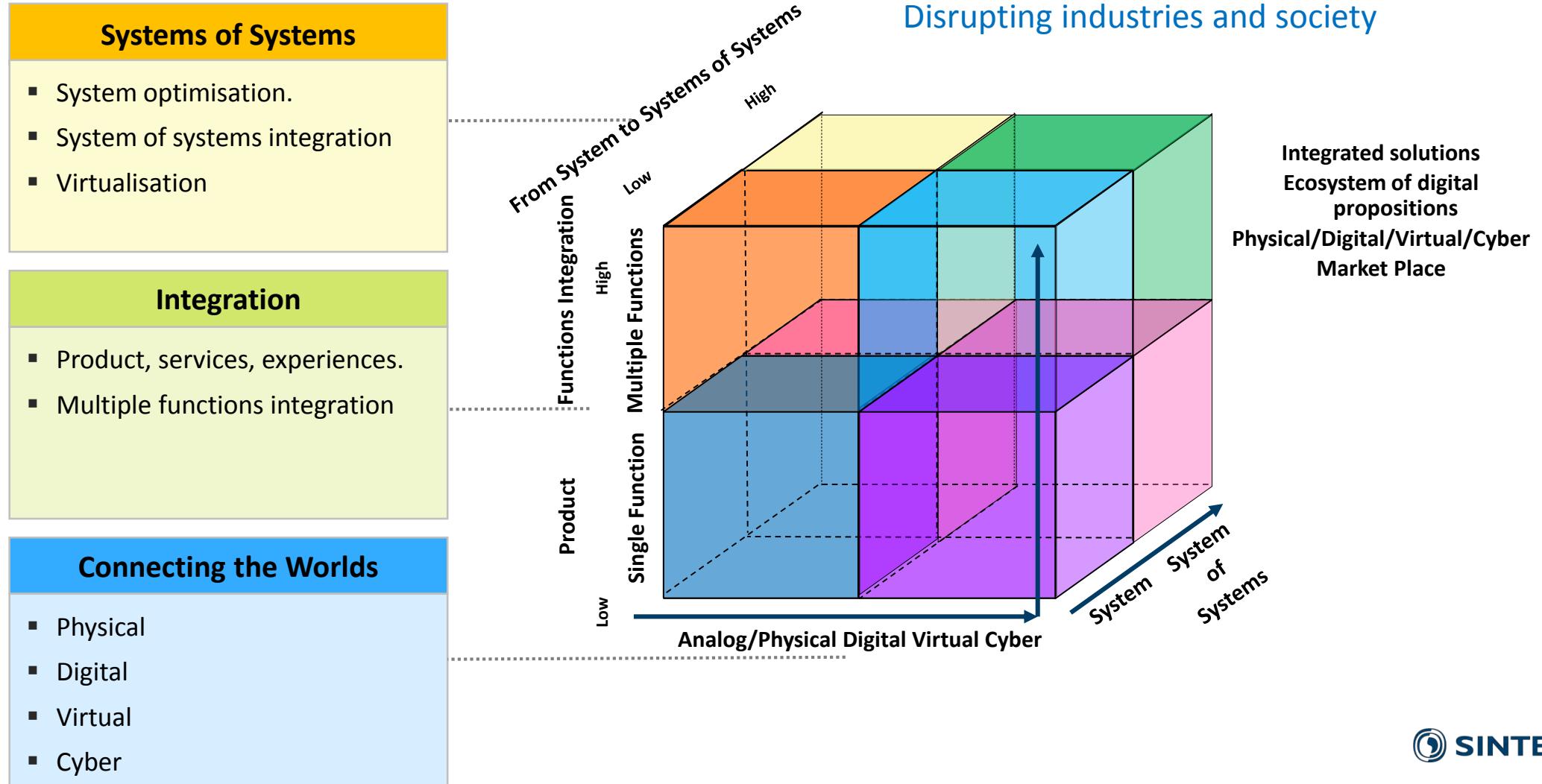


Dr. Ovidiu Vermesan, SINTEF, Oslo

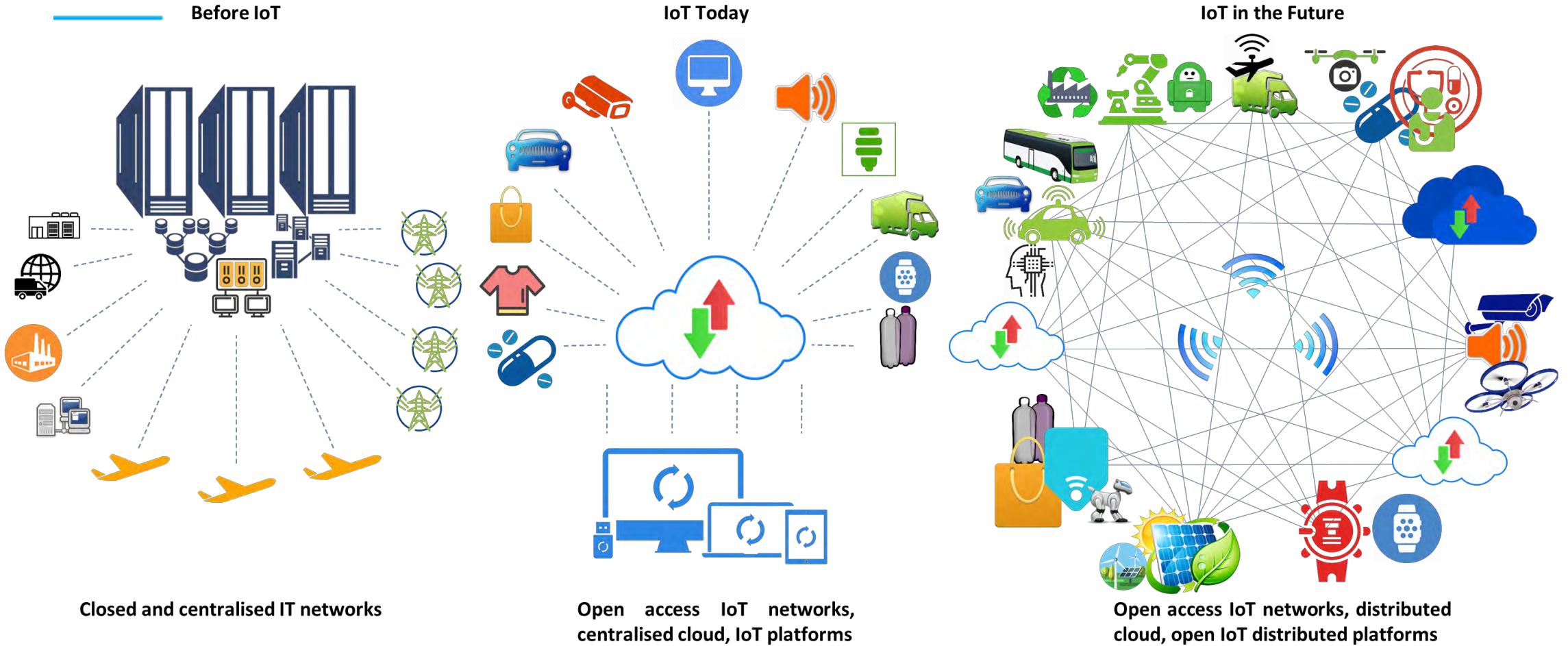
The dynamics of IoT digital age



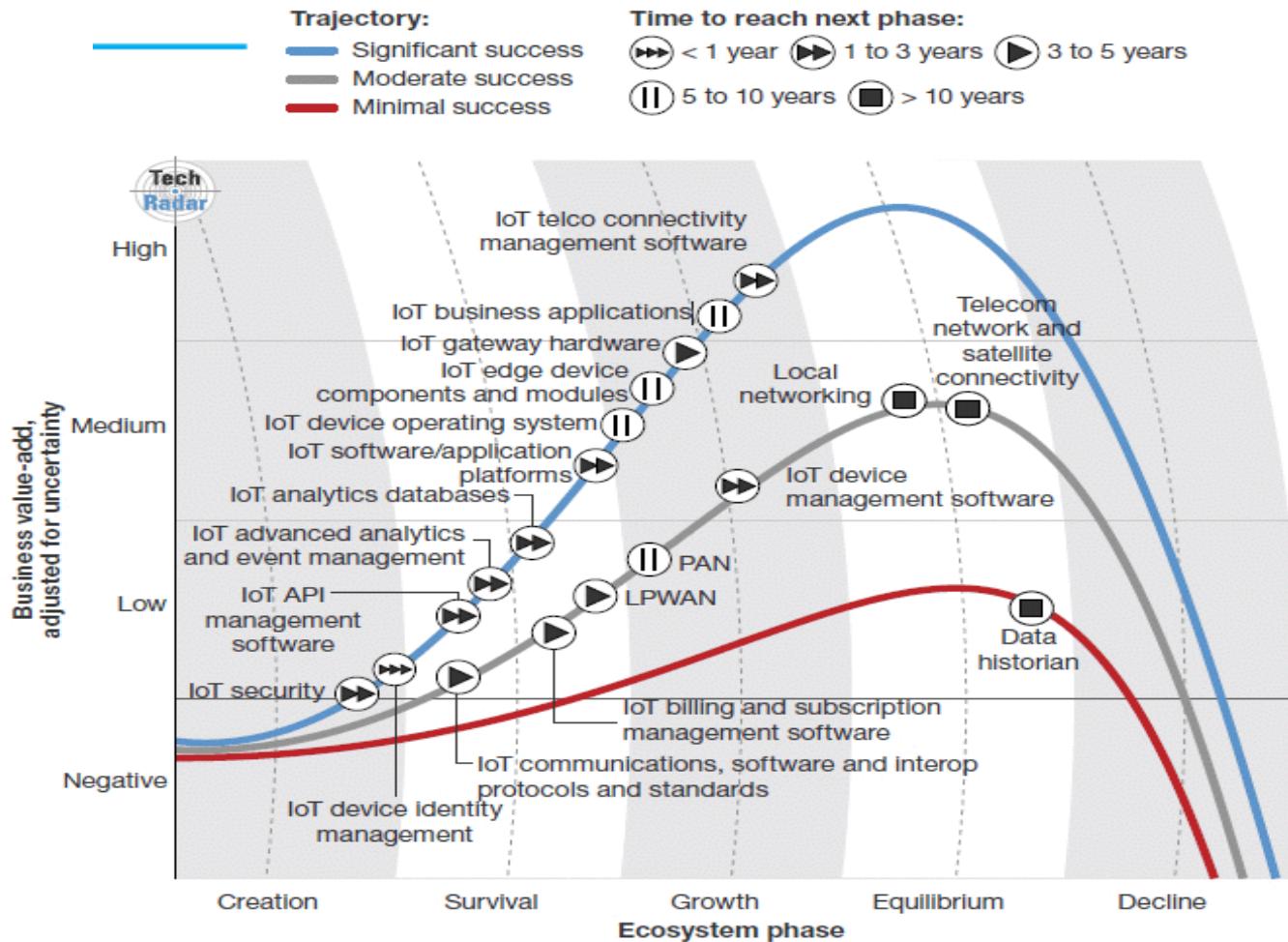
The pathway of IoT digital transformation



IoT development



IoT Development



IoT – Systems of Systems Integration

- Pervasive Sensing
- Cloud Computing
- Mobile Edge Computing

IoT Smart Digital Information

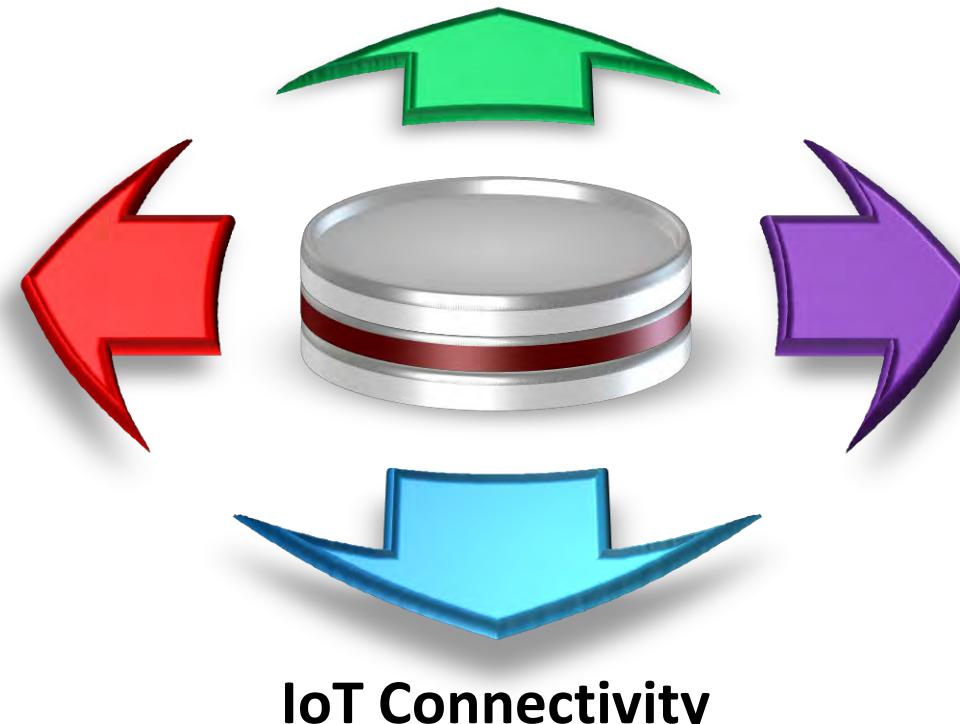
Capturing, processing and analysing IoT information allows predictions and decisions to be made

- Autonomous Systems
- Electricity Everywhere
- Digital Shadow

IoT Seamless Digital Access

IoT Internet access to customers, applications, platforms

- Physical, Digital, Virtual, Cyber
- Cognition
- Sustainability Energy Efficiency



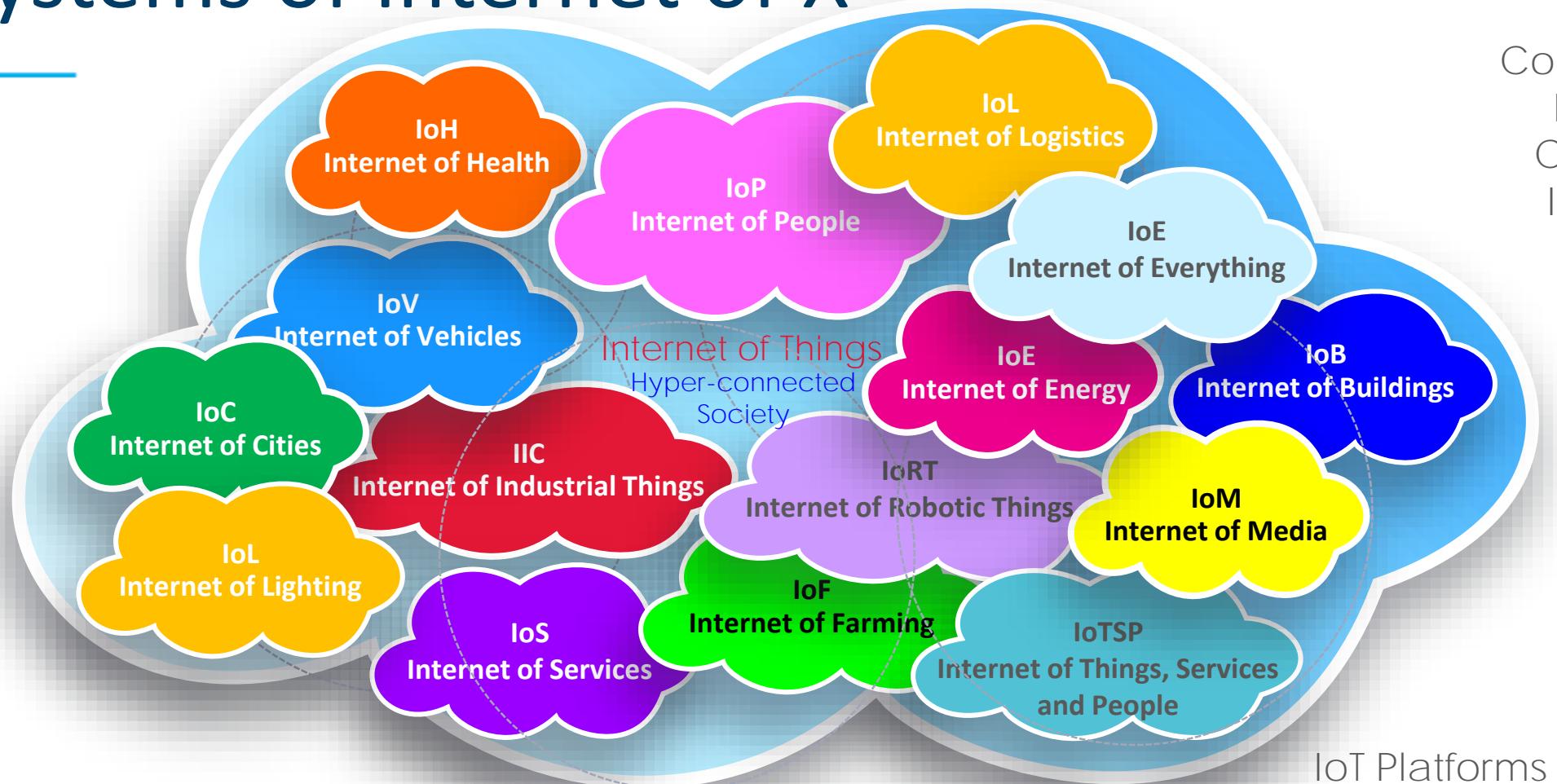
Interconnecting the entire value chain
Everything **connected**, everything **analysed**

Autonomous

Combining IoT technologies with artificial intelligence – generate systems that work autonomously and organize themselves.

- Smart Data
- Ubiquitous Mobile Computing
- Computer-aided Collaboration

Systems of Internet of X



IoT Architectural View

8 Collaboration and Processes Layer

(People and Business Processes)



7 Application Layer

Dynamic Applications
(Reporting, Analytics, Control)

- Health
- Energy
- Education
- Wearables
- Mobility
- Manufacturing
- Wellness
- Buildings
- Agriculture
- Environment
- Cities
- Smart Venues
- Security
- Transparency
- Privacy
- Integrity
- Trust
- Safety
- Ethics
- Dependability

6 Service Layer

(Services)

5 Abstraction Layer

Data Abstraction
(Aggregation and Access)

4 Storage Layer

Data Accumulation
(Storage)

3 Processing Layer

Edge Computing
(Data Element Analysis and Transformation)

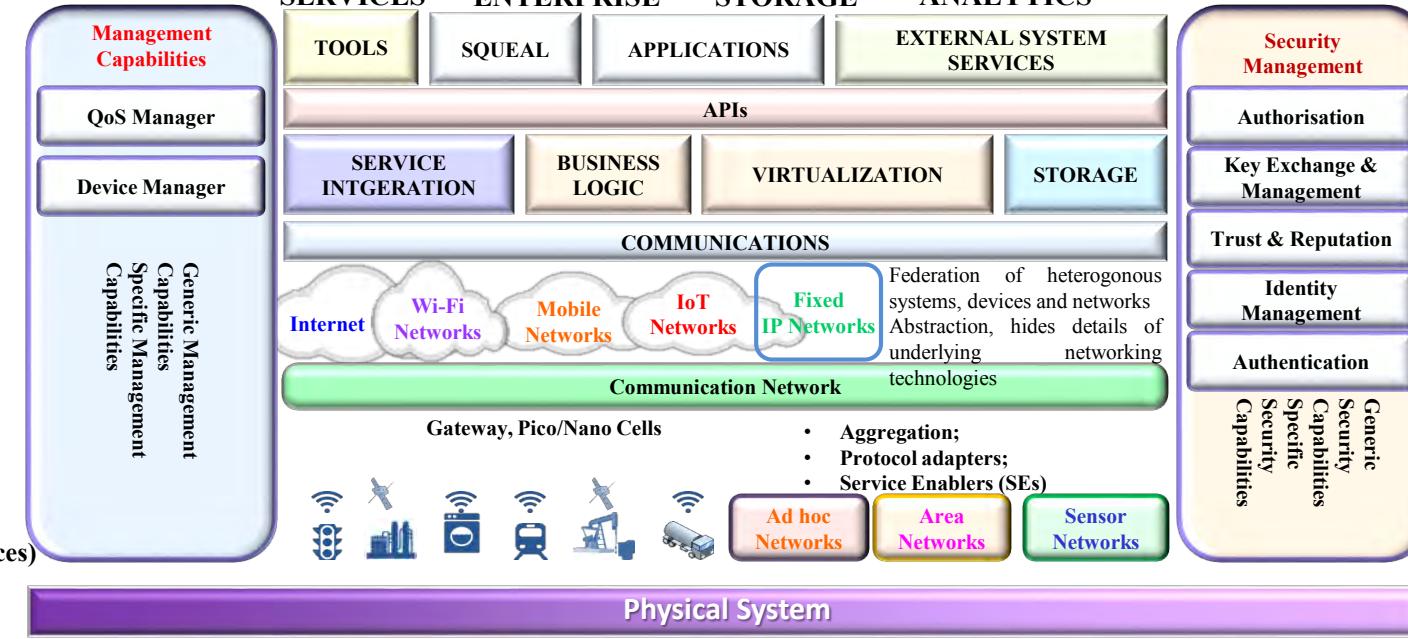
2 Network Communication Layer

Connectivity Elements Gateways
(Communication and processing units)

1 Physical Layer

Devices and Controllers
("Things" - Sensors/Actuators Wired/Wireless Edge Devices)

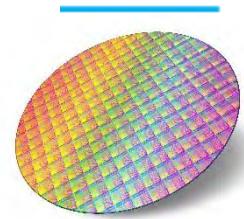
MULTI CLOUD SERVICES BUSINESS ENTERPRISE DISTRIBUTED STORAGE BIG DATA ANALYTICS



IoT Platform

- Reusable capabilities / enablers for applications
- Managed access to information based on secure, distributed and referable data
- Brokering for capability interaction plus export and import of enablers
- Support of different communication patterns
- Integration of different heterogeneous systems, devices and networks
- Abstraction, hides details of underlying (networking) technologies

IoT Neuromorphic Structure



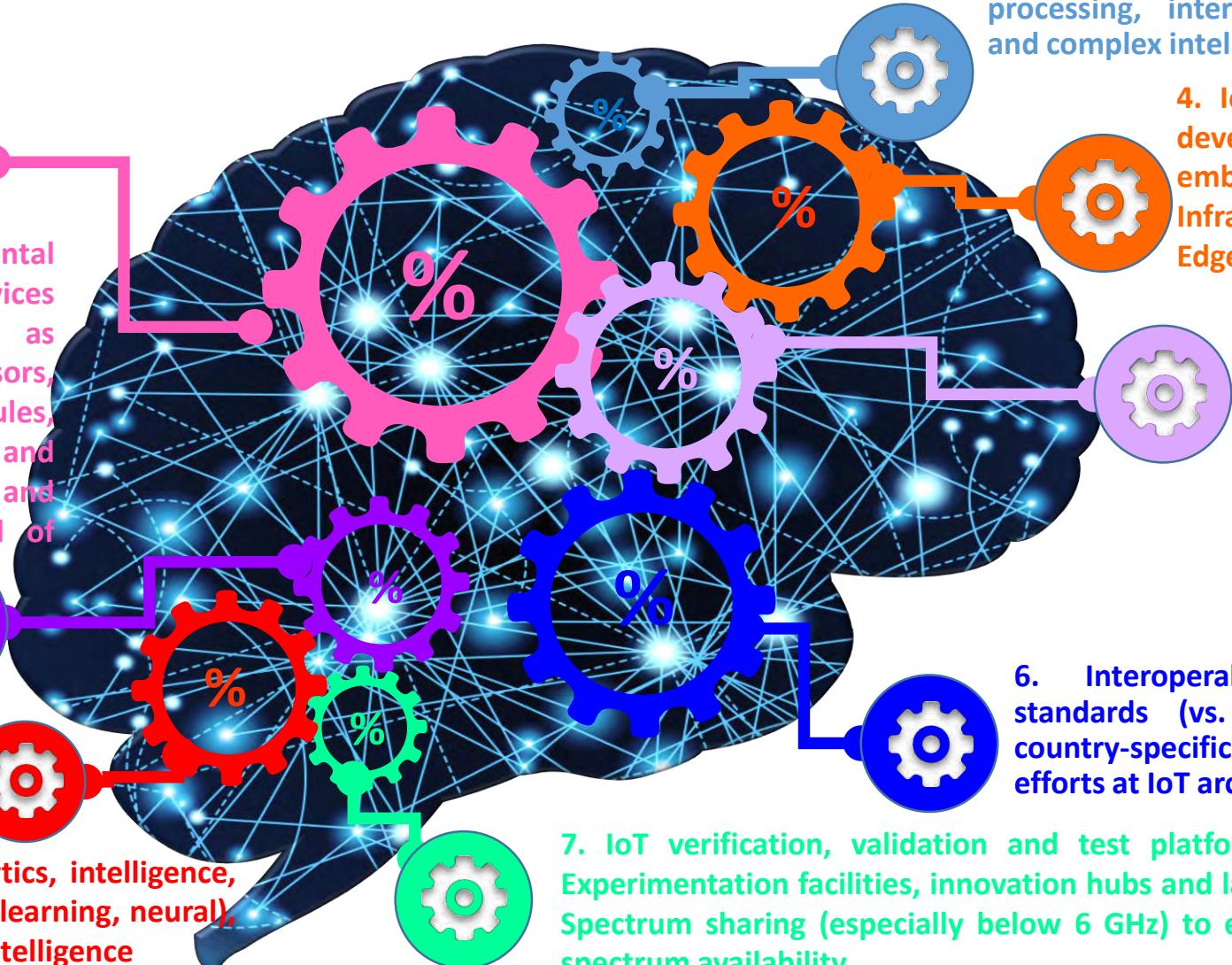
1. Semiconductors enabling technology. fundamental IoT devices require semiconductors such as microcontrollers, actuators/sensors, communication/connectivity modules, memory to connect to each other and perform their intended function, and the infrastructure to connect all of these devices.



3. Design, modelling, simulation, testing tools to accelerate IoT adoption and enable cost-effective introduction of new technologies.



9 8. Real time processing, analytics, intelligence, learning (M2M learning, deep learning, neural), collaborative and networked intelligence



2. Robust and dynamic IoT ecosystems that promotes key capabilities, connectivity, real time processing, interoperability, scalability, security, and complex intelligent analytics

4. IoT Platforms, open architectures, development tools, SDK, software (OS, embedded, application, etc.). Infrastructure PaaS, IaaS, SaaS, IoTaaS. Edge computing, fog, cloud.

5. Policy framework supporting trustworthy solutions based on horizontal building blocks, open architectures frameworks, that are scalable, interoperable, and reusable across deployments, providers, and sectors. Multi-level end-to-end, by design, by default security solutions.

6. Interoperability, industry-driven global standards (vs. proprietary solutions and/or country-specific standards), open standards efforts at IoT architectural and platforms levels.

7. IoT verification, validation and test platforms framework. Experimentation facilities, innovation hubs and large-scale pilots. Spectrum sharing (especially below 6 GHz) to enable increased spectrum availability.

IoT Architectural View

- 8 **Collaboration and Processes Layer**
(People and Business Processes)
- 7 **Application Layer**
Dynamic Applications
(Reporting, Analytics, Control)
- 6 **Service Layer**
(Services)
- 5 **Abstraction Layer**
Data Abstraction
(Aggregation and Access)
- 4 **Storage Layer**
Data Accumulation
(Storage)
- 3 **Processing Layer**
Edge Computing
(Data Element Analysis and Transformation)
- 2 **Network Communication Layer**
Connectivity Elements Gateways
(Communication and processing units)
- 1 **Physical Layer**
Devices and Controllers
("Things" - Sensors/Actuators Wired/Wireless Devices)



Holistic approach to secure, interoperable and scalable IoT platforms
Secure, safe, trustworthy, assured, resilient IoT components, systems and platforms.

Security

Redundant multi-level, end-to-end, by design, by default hardware and software security solutions

Dedicated security components and security features embedded into hardware and software components. Scalable, dynamic security solutions built into hardware at the transistor level as well as software, from the edge of the network, gateways, processing, to the servers in the cloud. Hardware- and software- level security capabilities to create redundancies to prevent intrusions and enable robust, secure, trusted IoT end-to-end solutions.

Analytics, intelligence, learning (M2M learning, deep learning, neural), collaborative and networked intelligence.

Advanced high computing server processors for processing, micro servers, virtualisation. New architectures for high performance computing.

Distributed computing architectures.

Intelligent storage and new memory technologies and improved management systems to store and archive the data in order to maximize its use and action-ability.

High performance microcontrollers for processing, micro servers, virtualisation. Storage. Memory at the edge level and multi-functional gateway level. Distributed computing paradigms/architectures for integrating clients, central nodes (cloud), gateways (fog) and edge nodes containing sensor/actuator interfaces.

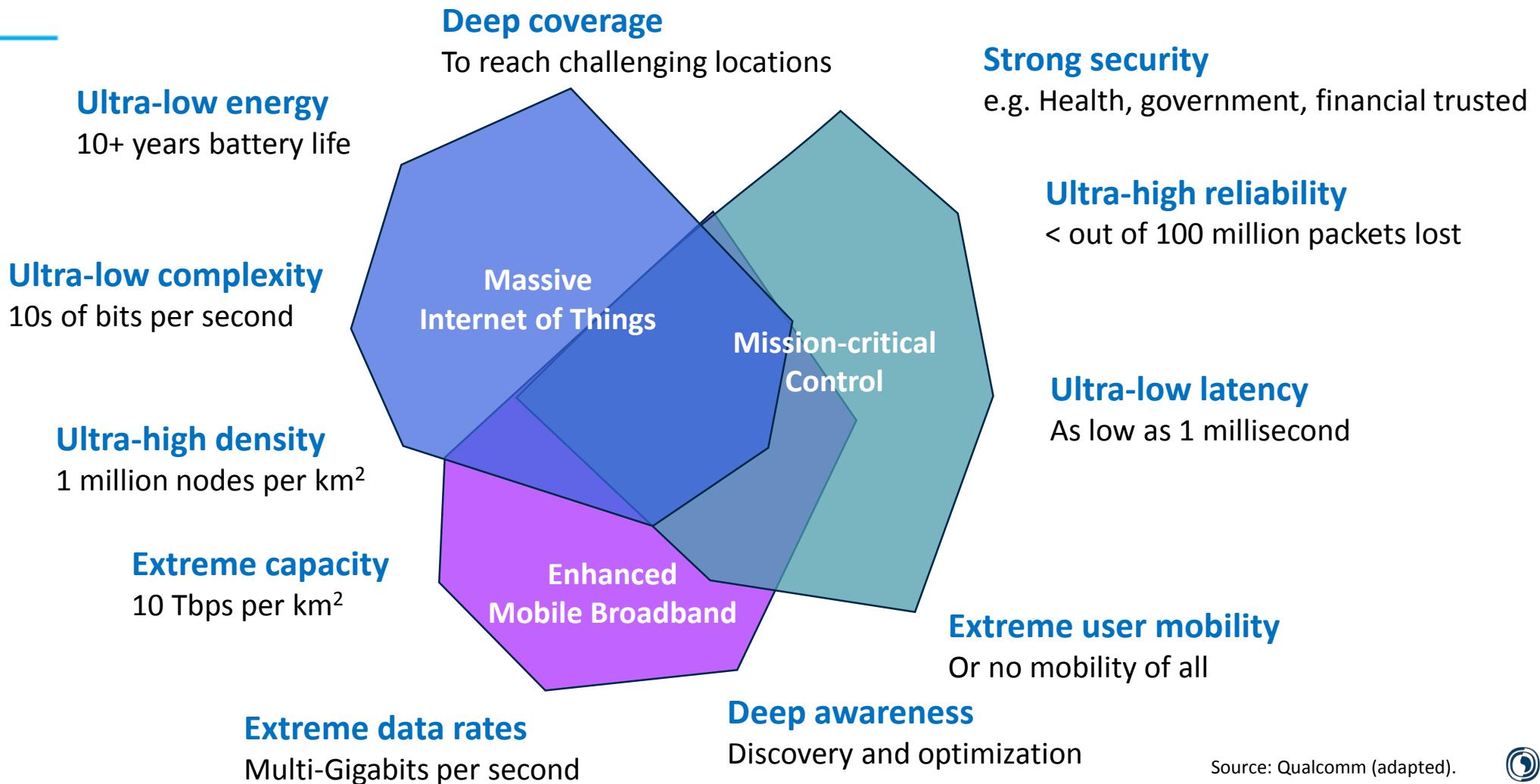
Enable universal connectivity. Ubiquitous broadband, high-speed broadband connections for advanced cellular technologies like 5G and advanced wireless technologies like next-generation Wi-Fi and NB-IoT, LoRa, SigFox, etc.. Networks virtualization. Multi-frequency, multi-protocol devices.

Energy-efficient sensing/actuating and computing. Sensor/actuator nodes that operate using battery power or harvesting energy from the environment. Ultra small, new processors developed with novel materials, devices, and computational and physical architectures that reduce the energy used to collect, move, analyse, and store data. Low, energy efficient integrated connectivity solutions. Storage. Memory at the node level.

Physical System

Energy efficiency at all levels, from the smallest sensor/actuator to ultra-high performance processors and algorithms.

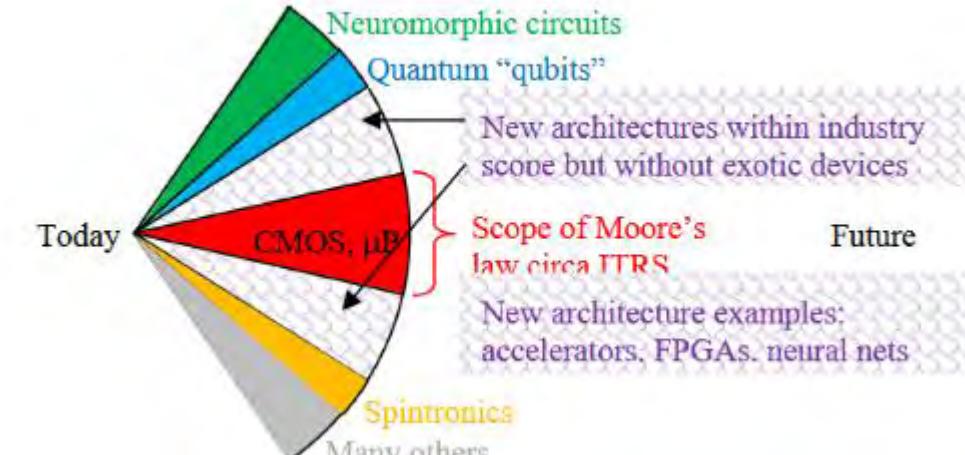
IoT and Connectivity Scalability



Semiconductor technology challenges

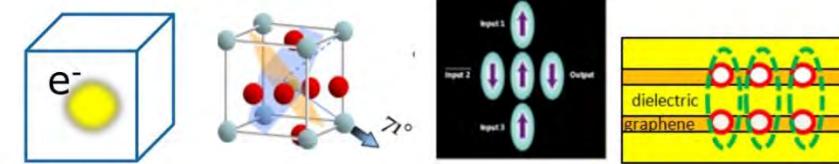
Table 1: Device architecture and ground rules roadmap for logic device technologies. $PxxMxx$ notation refers to Pxx : contacted poly pitch and Mxx : metalx pitch in nm. This shows the technology capability. On top of pitch scaling there are other elements such as cell height, vertical integration, fin depopulation, DTCO contracts, etc define the target area scaling (gates/mm²).

YEAR OF PRODUCTION	2015	2017	2019	2021	2024	2027	2030
<i>Logic device technology naming</i>	P70M56	P54M36	P42M24	P32M20	P24M12G1	P24M12G2	P24M12G3
<i>Logic industry "Node Range" Labeling (nm)</i>	"16/14"	"11/10"	"8/7"	"6/5"	"4/3"	"3/2.5"	"2/1.5"
<i>Logic device structure options</i>	finFET FDSOI	finFET FDSOI	finFET LGAA	finFET LGAA	VGAA, M3D	VGAA, M3D	VGAA, M3D
LOGIC DEVICE GROUND RULES							
MPU/SoC Metals ½ Pitch (nm) [1,2]	28.0	18.0	12.0	10.0	6.0	6.0	6.0
MPU/SoC Metal0/1 ½ Pitch (nm)	28.0	18.0	12.0	10.0	6.0	6.0	6.0
Contacted poly half pitch (nm)	35.0	24.0	21.0	16.0	12.0	12.0	12.0
L_g Physical Gate Length for HP Logic (nm) [3]	24	18	14	10	10	10	10
L_g Physical Gate Length for LP Logic (nm)	26	20	16	12	12	12	12
Channel overlap ratio - two-sided	0.80	0.80	0.80	0.80	0.80	0.80	0.80
Spacer width (nm)	12	8	6	5	4	4	4
Contact CD (nm) - finFET, LGAA	22	14	16	12	11	11	11
<i>Device architecture key ground rules</i>							
FinFET Fin Half-pitch (nm) = 0.75 or 1.0 M0/M1 (nm)	21.0	18.0	12.0				
FinFET Fin Width (nm)	8.0	6.0	6.0				
FinFET Fin Height (nm)	42.0	42.0	42.0				
Footprint drive efficiency - finFET	2.19	2.50	3.75				
Lateral GAA Lateral Half-pitch (nm)			12.0	10.0			
Lateral GAA Vertical Half-pitch (nm)			12.0	9.0			
Lateral GAA Diameter (nm)			6.0	6.0			
Footprint drive efficiency - lateral GAA, 3x NWs stacked			2.4	2.8			
Vertical GAA Lateral Half-pitch (nm)				10.0	8.0	6.0	6.0
Vertical GAA Diameter (nm)				6.0	5.0	5.0	5.0
Footprint drive efficiency - vertical GAA, 3x NWs stacked				2.8	3.9	3.9	3.9
Device effective width - [nm]	92.0	90.0	56.5	56.5	56.5	56.5	56.5
Device lateral half pitch (nm)	21.0	18.0	12.0	10.0	6.0	6.0	6.0
Device width or diameter (nm)	8.0	6.0	6.0	6.0	5.0	5.0	5.0

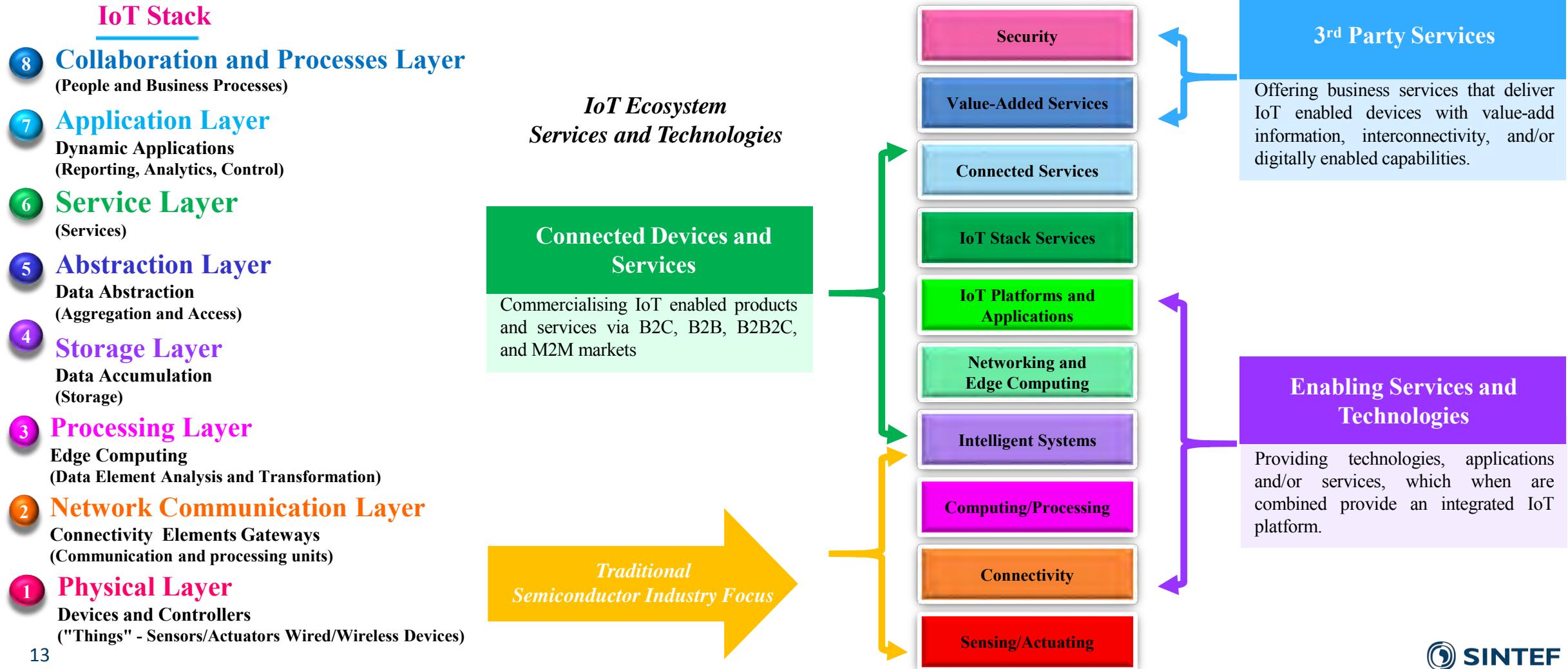


Computational Variables

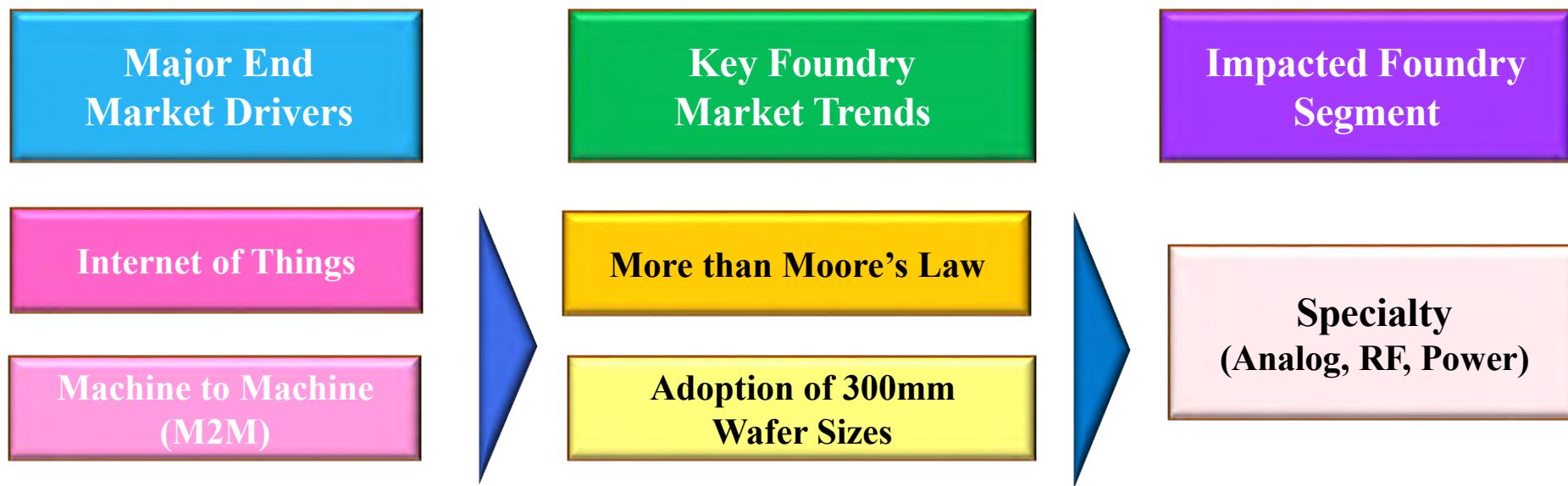
Class	Variables	Example
Charge	Q, I, V	CMOS, TFET
Electric Dipole	P	(FeFET)
Magnetic Dipole	M, I_{spin}	All-Spin Logic (ASL), SpinWave Device (SWD), NanoMagnetic Logic (NML)
Orbital State	Bose condensate	BisFET



Semiconductor device makers function



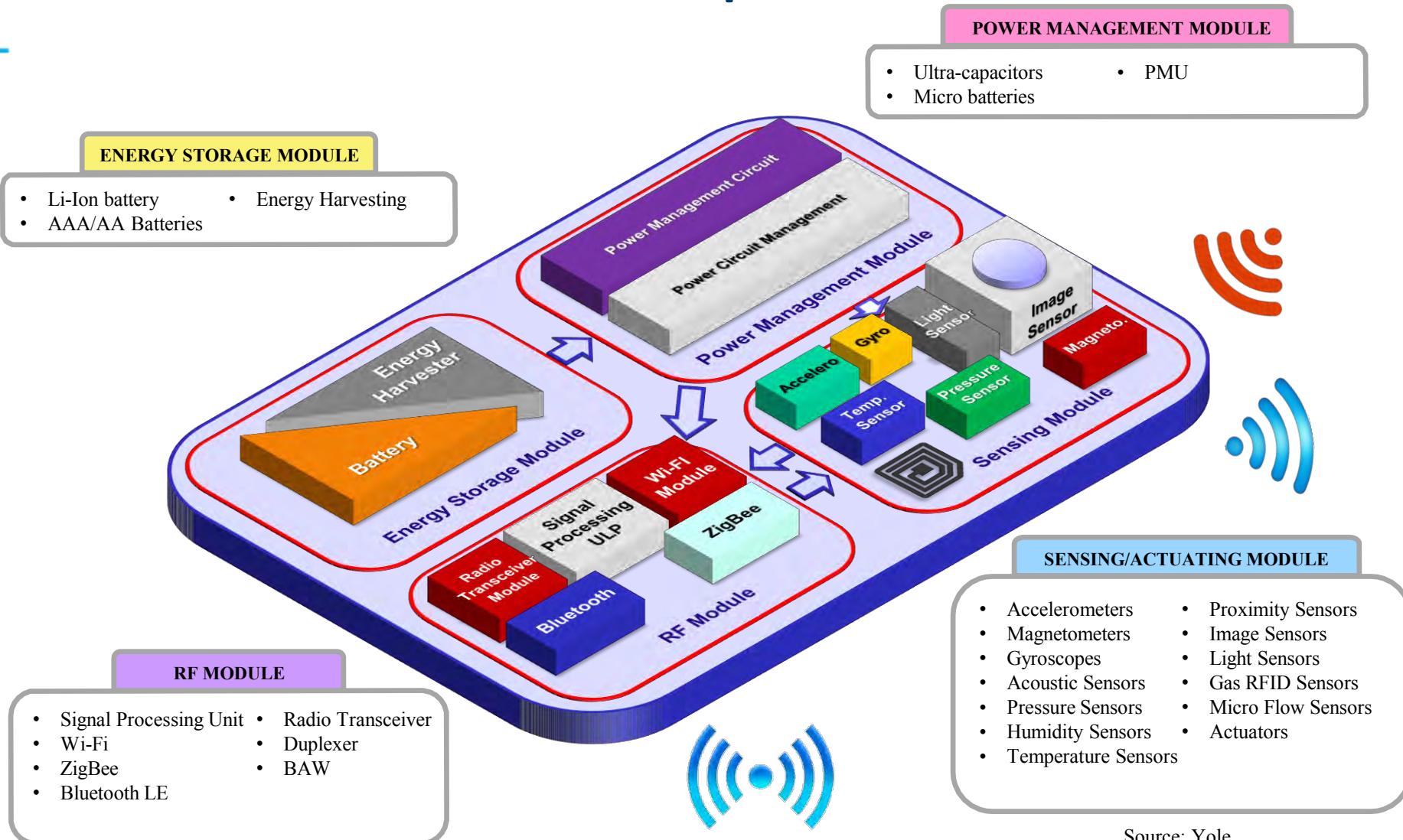
Specialty foundry market size



Market size for specialty devices especially in automotive and industrial segments will increase

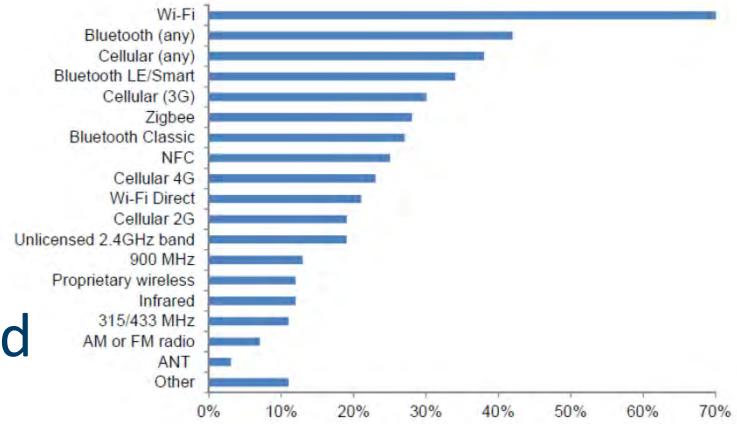
The increase in number of fabless customers and specialty foundry applications could lead to adoption of 300mm wafer sizes

IoT sensor/actuator map



IoT Technology - Communication

- Ethernet
- Power Line Communications
- Bluetooth/BLE
- 802.15.4/Zigbee/WirelessHART/ISA100.11a/Thread
- Z-Wave
- Wi-Fi
- LPWA (SIGFOX, LoRa, RPMA, Weightless, Telensa, Qowisio)
- GSM (GPRS, EDGE, EC-GSM)
- LTE (Cat-4, Cat-1, Cat-0, Cat-M1)
- NB-IoT (3GPP Cat-NB1)
- 5G IoT
- Satellite



IoT Technology - Communication

- WIRED IoT
 - Ethernet+Fieldbus
 - Power Line Communications
 - PRIME and G3-PLC
- SHORT-RANGE WIRELESS IoT
 - Bluetooth and BLE
 - RFID
 - 802.15.4
 - Zigbee
 - WirelessHART
 - ISA100.11a
 - Thread
 - 802.11
 - Wi-Fi a-n
 - Wi-Fi Direct



- ❖ LPWA
 - LPWA
 - *LoRa*
 - *SigFox*
 - *RPMA*
 - *UNB*
 - *Weightless*
 - *Qowisio*
 - ❖ GSM AND EC-GSM
 - GSM, EDGE, and EC-GSM
 - ❖ LTE AND NB-IoT
 - LTE *Cat-4, Cat-1, Cat-0, and Cat-M1*
 - NB-IoT
 - ❖ 5G IoT
 - 5G IoT
 - ❖ SATELLITE IoT
 - Satellite IoT
 - Globalstar, Inmarsat, Iridium

36 Different Connection Technologies



IoT Connected Device Shipments
IoT Device Revenue
Enterprise vs. Govt. vs. Consumer

17

Connectivity

Coverage Range	1-100 m	100 m – 10 km	10 km – 100 km
Examples of standards applied in WAN: Wide Area Networks		 LoRa  ETSI EN 300 220-1  EIGHTLESS™  GSM 3G 4G LTE  GPRS  WiMAX	
Examples of standards applied in NAN: Neighborhood Area Network	 GREEN PHY by ETSI GxPLC Alliance	 ISA 100.000 WIRELESS  WirelessHART  ZigBee®  M-Bus  DECT  Wi-Fi  LoRa  ETSI EN 300 220-1  EIGHTLESS™  GSM 3G 4G LTE  GPRS  WiMAX	
Examples of standards applied in LAN: Local Area Networks	 VLC  KNX  ZigBee®  wavenis	 ISA 100.000 WIRELESS  M-Bus  DECT  Wi-Fi  LoRa  ETSI EN 300 220-1  EIGHTLESS™  4G LTE	
Examples of standards applied in HAN: Home Area Network	 Modbus  NFC  RFID  4G  Bluetooth  VLC  KNX  ZigBee®  enocean  LowPAN  Wi-Fi  4G LTE  z-wave		
Examples of standards applied in PAN: Personal Area Networks	 NFC  VLC  RFID  4G  Bluetooth  ZigBee®  Wi-Fi		
Examples of standards applied in BAN: Body Area Network	 NFC  VLC  RFID  4G  Bluetooth  ZigBee®		

WAN: Wide Area Networks cover a broad area, communication links that cross metropolitan, regional, or national boundaries. Internet is an example of a WAN.

NAN: Neighborhood Area Network is defined as a utility access network that connects meters and distribution automation devices to WAN gateways such as RF collectors or data concentrators and field devices.

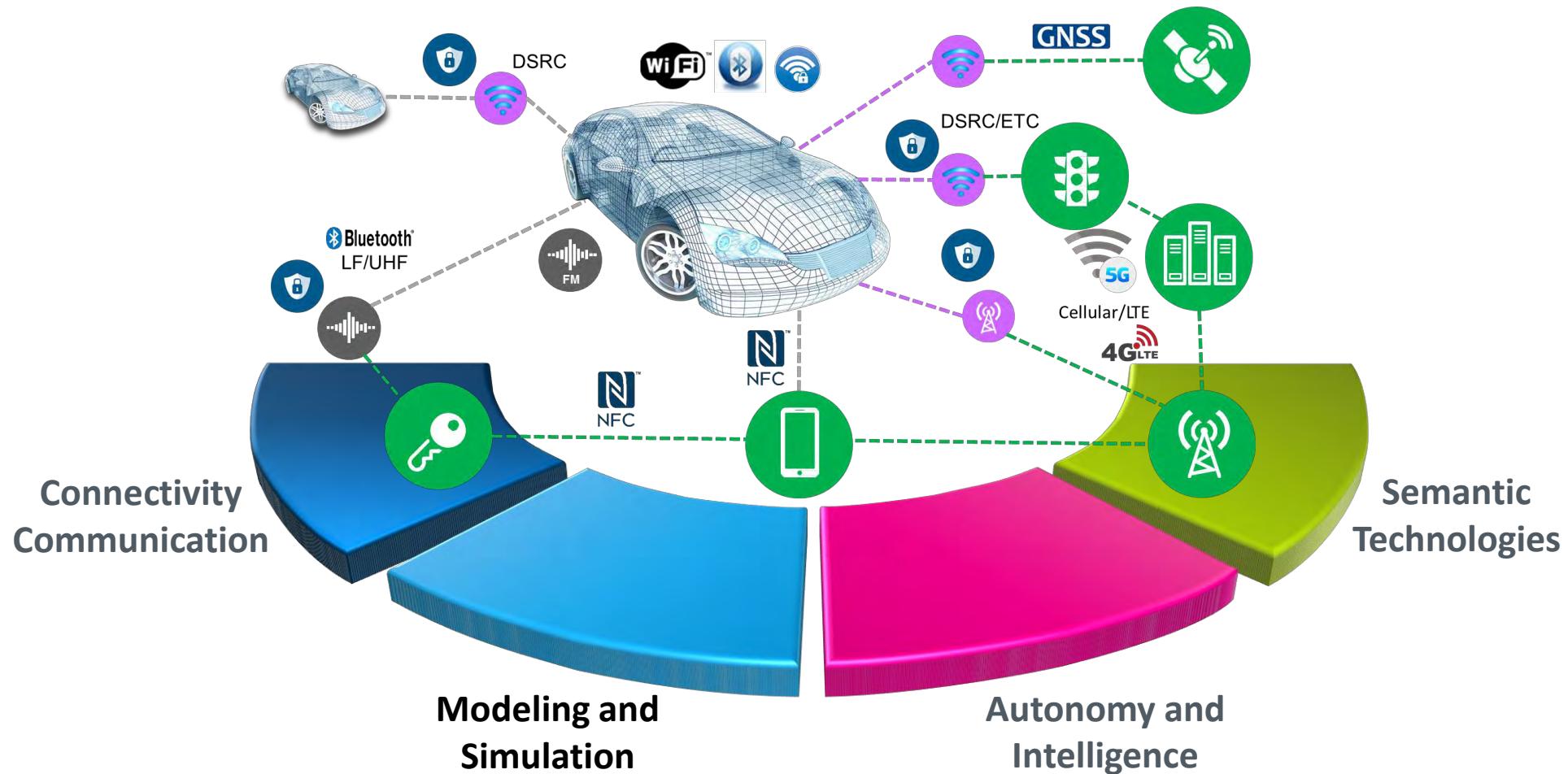
LAN: Local Area Networks cover a small physical area, like a home, office, or a small group of buildings or facilities. WLAN: Wireless Local Area Networks cover wirelessly a larger area that is connected to the network.

HAN: Home Area Network is the connection of network enabled devices in a domestic home.

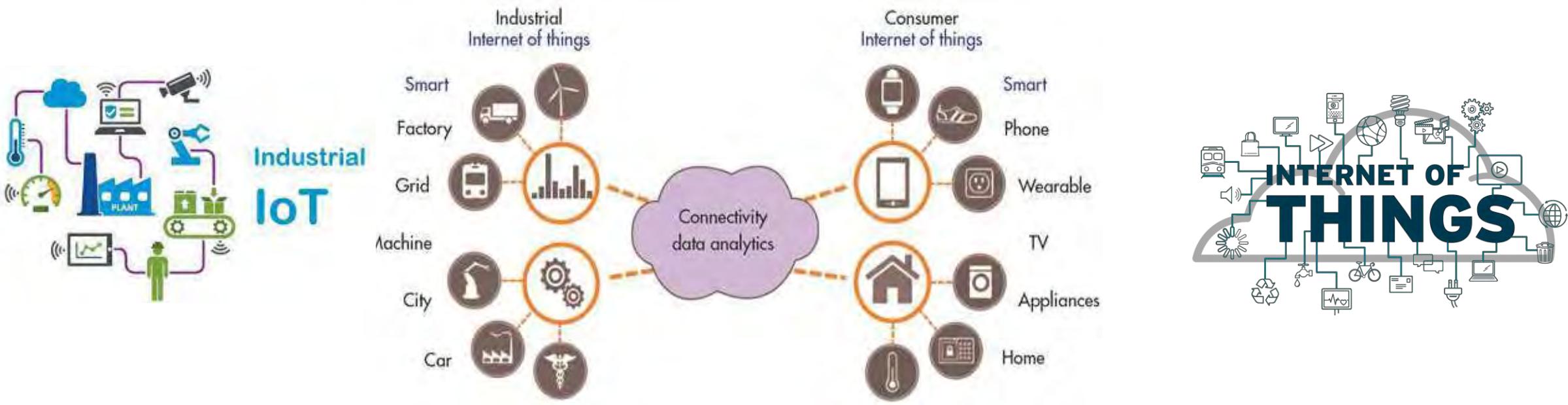
PAN: Personal Area Networks are used for communication among various devices, such as phones, personal devices, fax, and printers, that are located close to a single user.

BAN: Body Area Network comprises communication between sensors placed on the human body.

IoT pervasion of technologies



IoT Consumer-Industrial-Business Convergence



Source: Designing the Industrial Internet of Things, electronicdesign.com.

Internet of Robotic Things IoRT

- IoT and robotics technologies combine to provide for Ambient Sensing, Ambient Intelligence and Ambient Localization, which can be utilized by new classes of applications to deliver value.



Security - convergence

IoT Security

OT Security

IT Security

Dependent Devices

- Authentication
- Authorisation
- Communication Encryption
- Monitoring
- Snooping – Data Leakage
- Spoofing

Quasi Intelligent Devices

- Access Control
- Data Encryption
- Firmware security
- Patch Management
- Intrusion Detection

Intelligent Devices and Controllers

- Device Provisioning
- OS Security



SIEM and Analytics
Security Information and Event Management

Data Security

Application Security

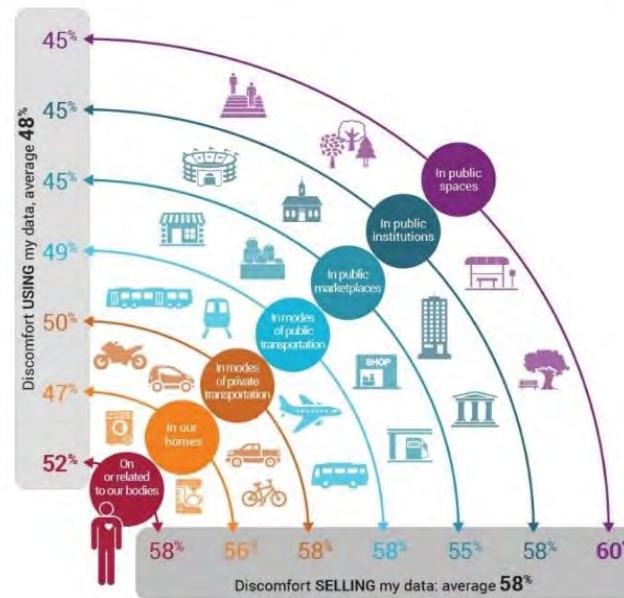
Network and Communications

Device Authentication
Access Provisioning and Monitoring

Information technologies: conventional computers, operating systems, networking components and software platforms.

Operational technologies: industrial control system and networks.

ROUGHLY HALF OF ALL CONSUMERS HIGHLY UNCOMFORTABLE WITH COMPANIES USING AND SELLING THEIR DATA IN PHYSICAL SPACES
Q. How comfortable are you with companies USING vs. SELLING your data in each of the following areas, assuming you have opted-in to their products/services.



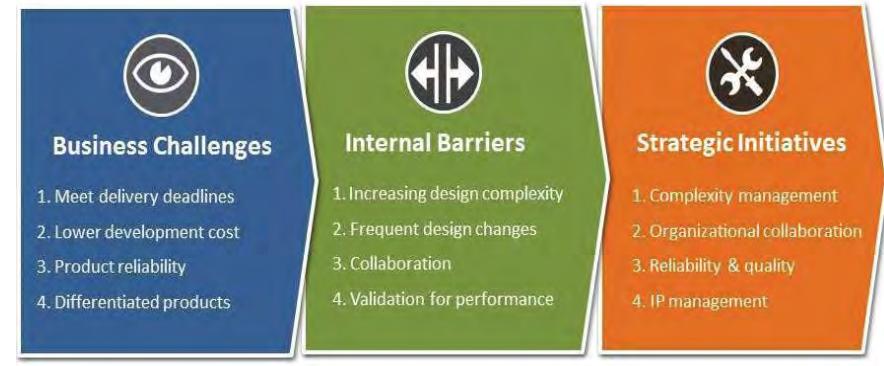
Note: These percentages reflect all respondents who, on a scale of 1-5 rated their comfort level as a 1 (Extremely uncomfortable) or 2 (Uncomfortable) with companies using vs. selling their data across each physical space.
Source: Consumer Perceptions of Privacy in the Internet of Things, Altimeter Group, 2015. Base: n=2062 respondents

ALTIMETER

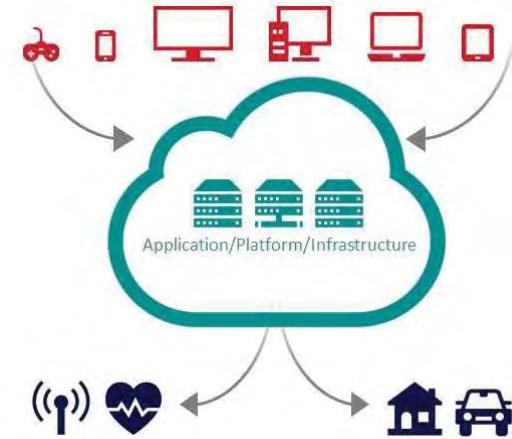
Breaking down walls

Convergence of devices

- The convergence of technology from formerly differentiated domains (e.g., consumer, computer, industrial, and automotive) requires a look at new challenges and new use models.
 - The connection of electronic products to the Internet, producing and consuming large amounts of information.
 - The need to design more devices to produce and consume information autonomously, independent of humans.
 - The need for companies, engineers, and technologies to constantly change and adjust the way in which products are being developed.

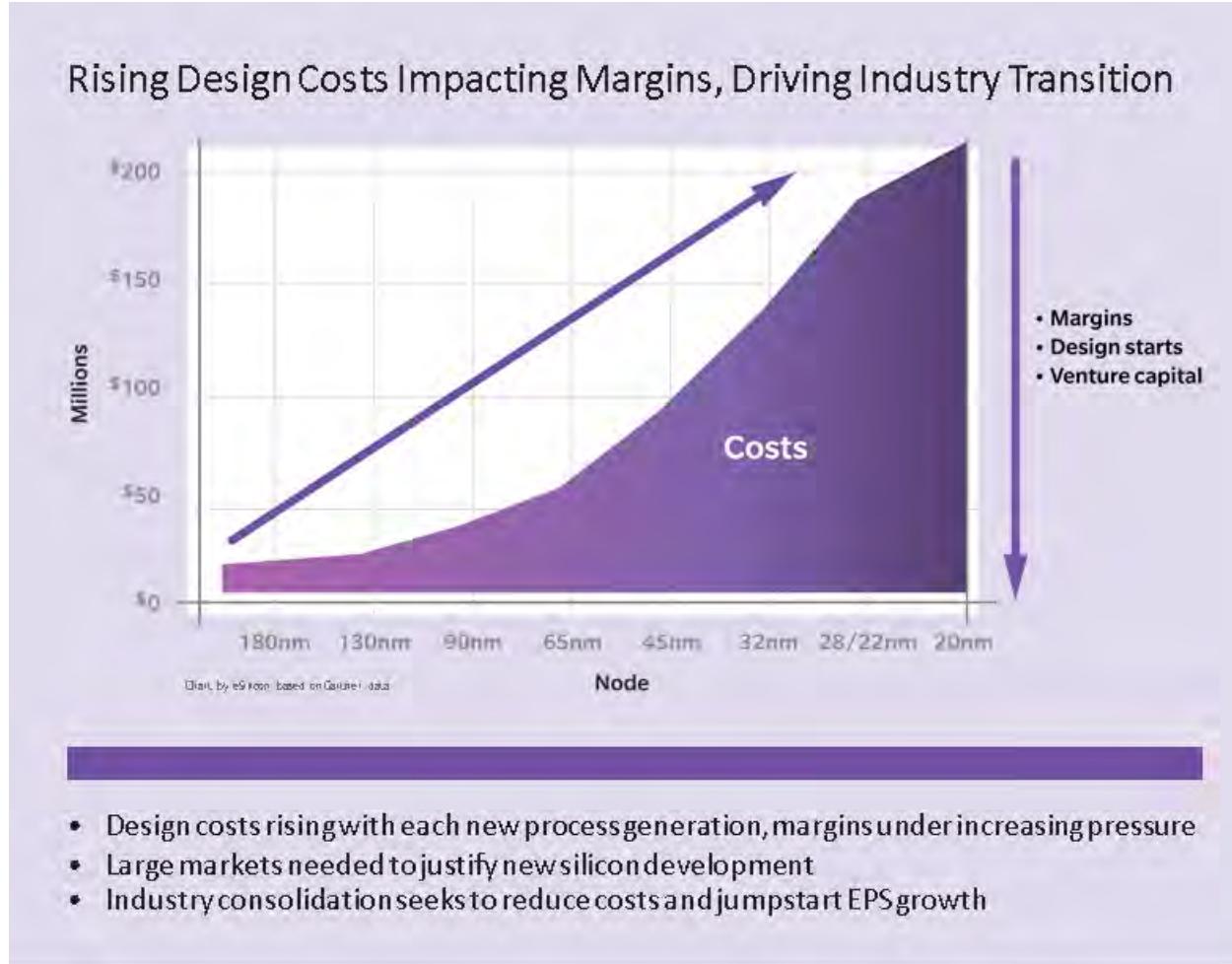


Execution through Strategic Initiatives



Source: 2016 BY Mentor Graphics

Rising Chip Design/Manufacturing Costs

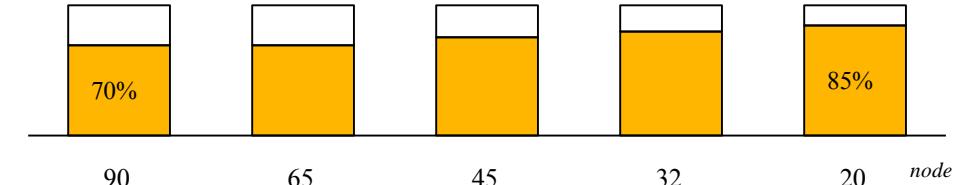


Cost increasing cost pressures



Equipment cost rising to 85% of total fab costs

Facilities Equipment

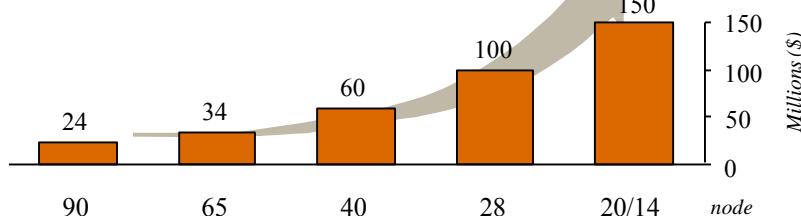


Device

Packaging

- 2D transistor -> 3D FinFET
- Advanced Materials for 14nm and beyond
- 2D->2.5D -> 3D
- 3D is 1.4x more expensive than 2D packaging

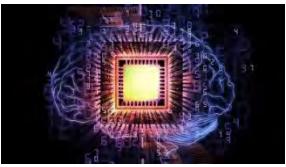
Chip development Cost rapidly escalating



* Morgan Stanley estimate of the cost increase in shift from 65nm to 20 nm

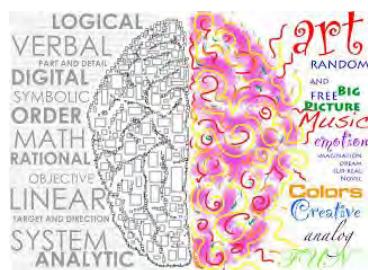
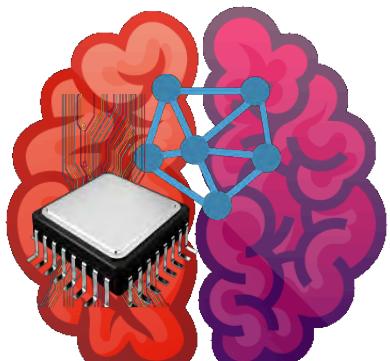
Source: PwC

Computing paradigm change



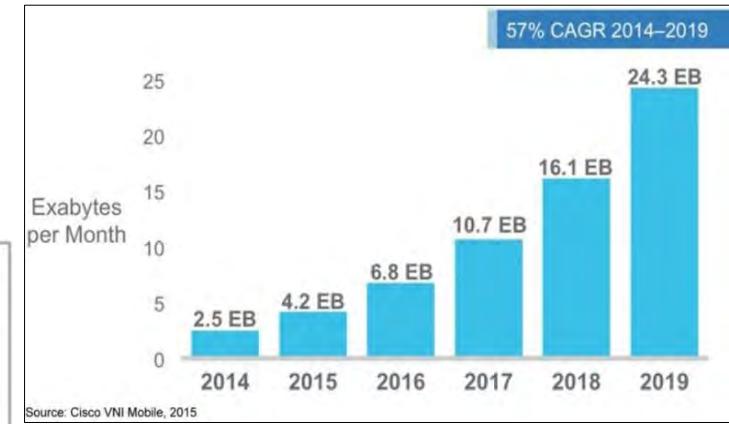
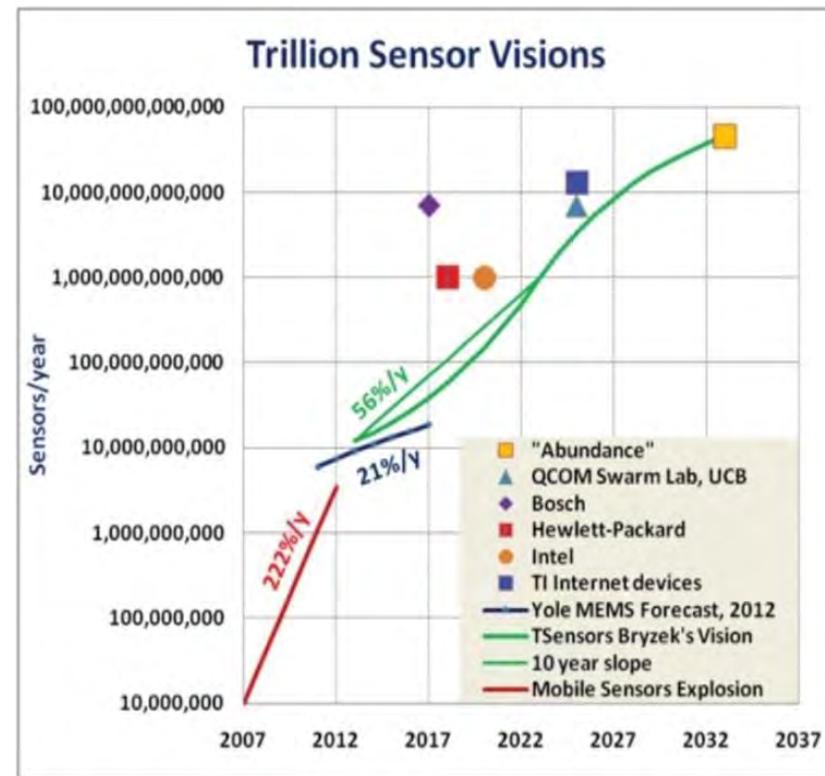
Computers

- Serial architecture**
- High power (100MW), Large footprint (40M litter)
 - Synchronous (master clock)
 - Digital computing
 - Digital communication
 - Memory and computation are separated
 - Intelligence via operating systems, programmed algorithms, rules.
 - Built with precise components operating at high speeds (GHz)
 - Active when instructed

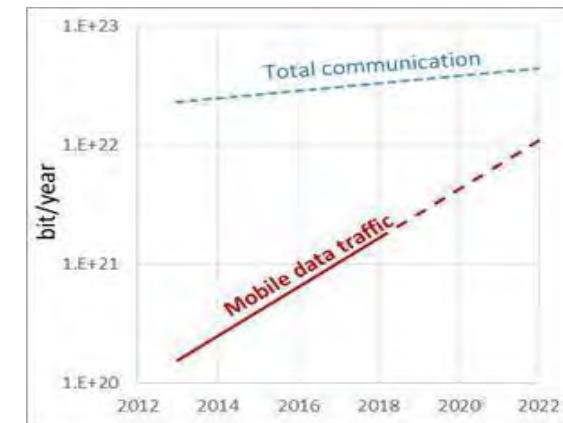


Mammalian Brains

- Parallel distributed architecture**
- Low power (25W), small footprint (1 litter)
 - Asynchronous (no master clock)
 - Analog computing
 - Digital communication
 - Integrated memory and computation
 - Intelligence via learning through brain-body-environment (BBE) interactions.
 - Built with noisy components operating at low speeds (<10Hz)
 - Unpredictable active



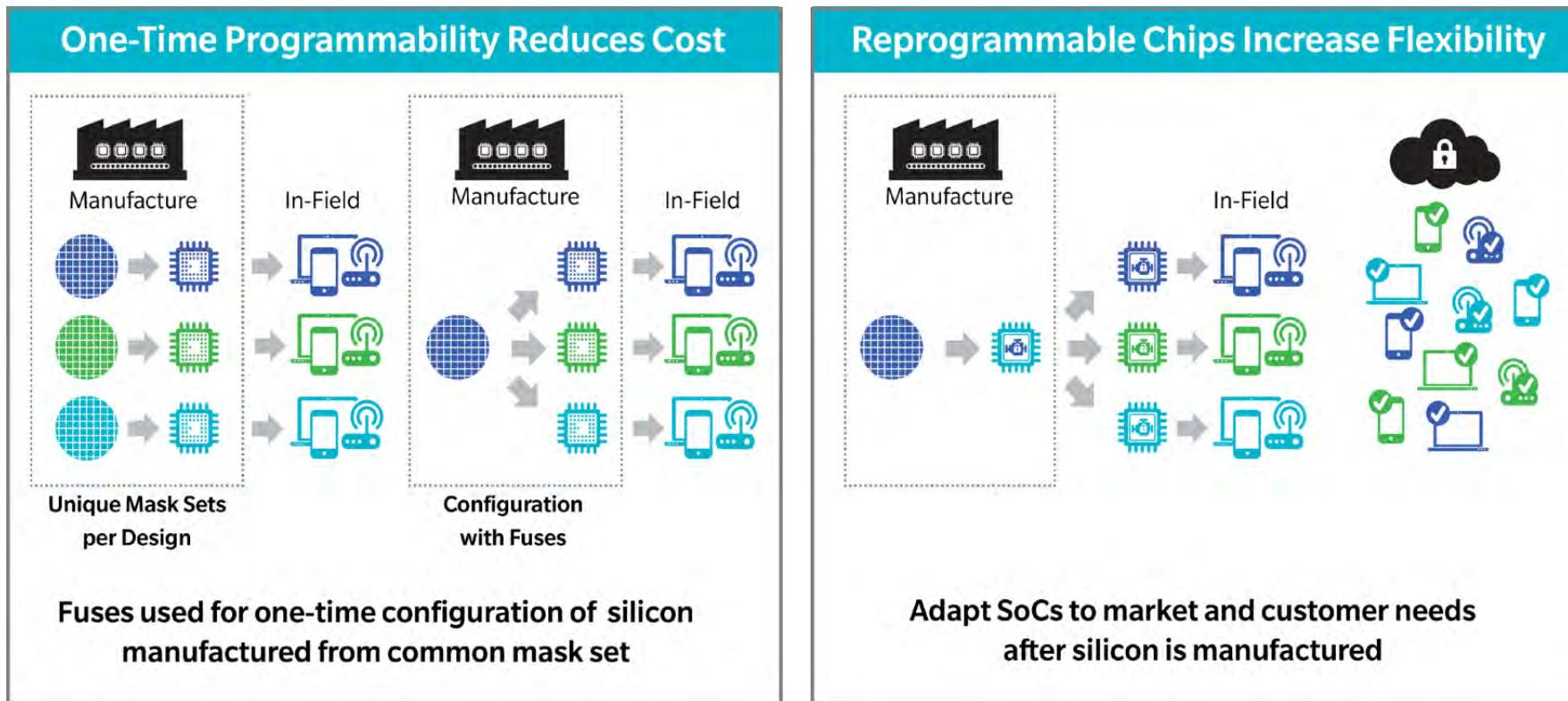
Annual growth of mobile data traffic



Mobile data traffic vs. total communication

Source: 2015 Semiconductor Industry Association.

Reprogrammable chips

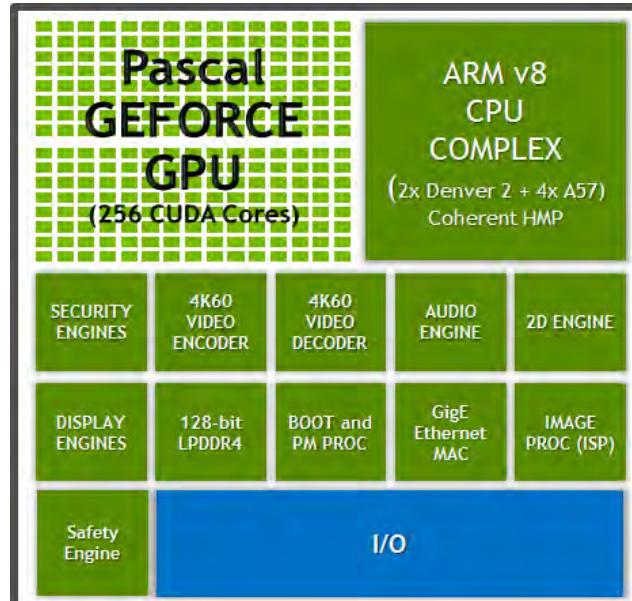


- Reprogrammable silicon improves security and flexibility, enables downstream configuration
- Features as a Service (FaaS): Features and Services can be enabled and disabled in-field, opening up a wide range of new usage model

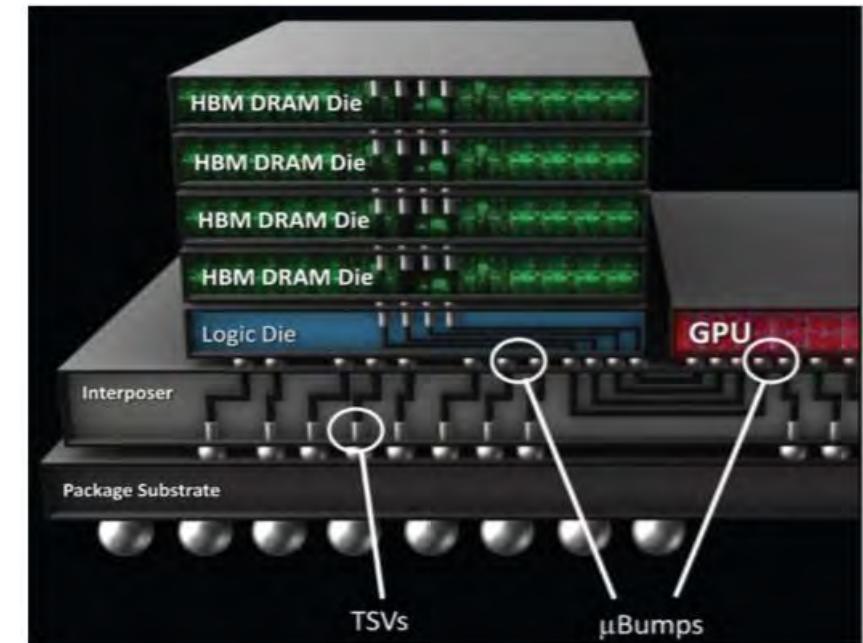
Alternative to x86, ARM Architectures

- Shifting to Photonics

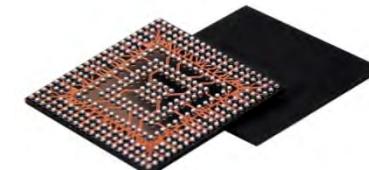
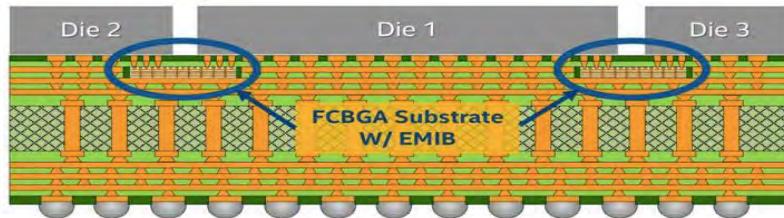
Device is built for throughput and speed. So while it includes a 16nm finFET-based CPU, with two 64-bit “Denver” ARM cores and four A57s, it also includes a number of other chips, including a 256-core GPU, display and audio engines, a safety engine, a secure boot processor, and DDR4 memory



Nvidia's Parker chip architecture. Source: Nvidia.

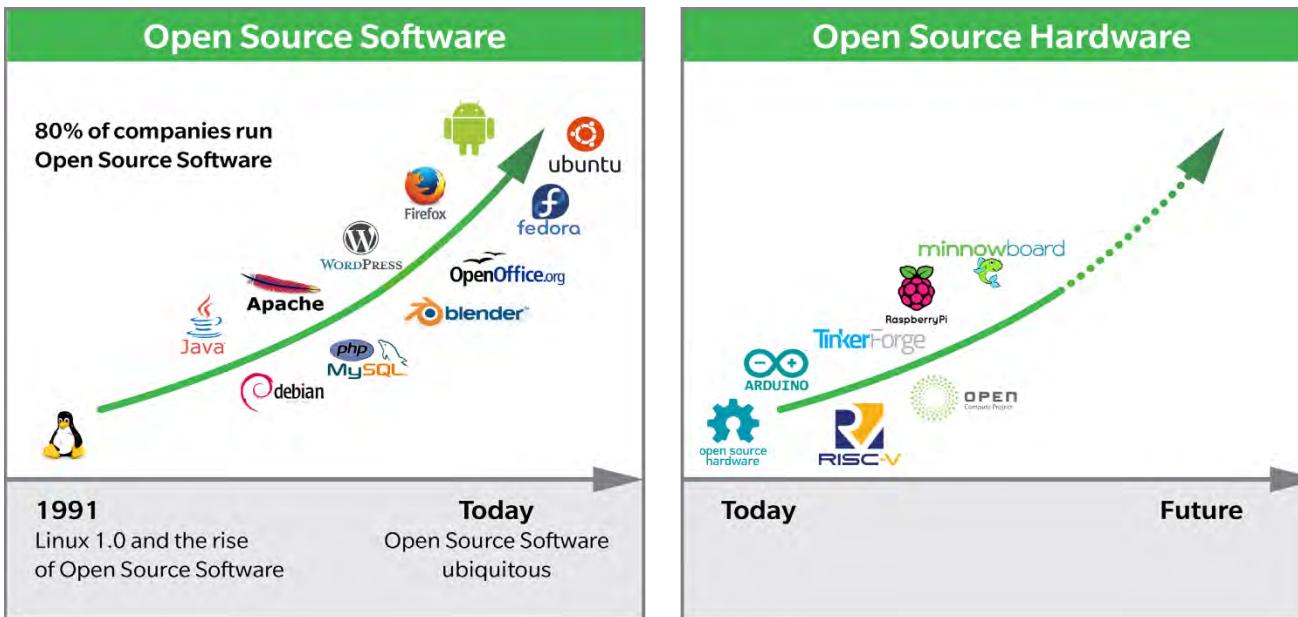


AMD's Fiji solution. Source: TechSearch International/AMD



Open-Source Semiconductors

- Open Source Software - Hardware



Google, Hewlett Packard Enterprise, Lattice Semiconductor, Microsemi and Oracle signed up as initial members of RISC-V trade group.

A R&D division of the Indian government is slated to receive approximately \$45 million to fund the development of its first 64-bit microprocessor based on the RISC-V instruction set.

Designers at IIT Madras has been working for more than two years on a family of 32- and bit open source processors based on RISC-V, called Shakti. According to EE Times, the Shakti project now includes plans for at least six microprocessor designs along with fabrics and an accelerator chip.

- Rapid growth in the adoption and number of open source software projects
- More than 95% of web servers run Linux variants, approximately 85% of smartphones run Android variants
- Will open source hardware ignite the semiconductor industry? Is RISC-V the hardware industry's Linux?



Technology for a better society