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Welcome to NEREID

2016/02/02

Welcome to the NEREID project's website. The NEREID project ("NanoElectronics Roadmap for Europe: Identification and Dissemination") is dedicated to map the future of European Nanoelectronics. Started in November 2015, NEREID is a Cooperation and Support action that has received funding for three years from the European Union's Horizon 2020 research and innovation programme under grant agreement No 685559.

<< **Hint: The final version of the NEREID roadmap is available here.** >>

NEREID presents its final Roadmap

2018/12/14

The H2020-CSA project "NanoElectronics Roadmap for Europe: Identification and Dissemination" (NEREID) here presents its final roadmap, the printed book of the "NanoElectronics Roadmap for Europe" which is already [available here](#).



NEREID present at EF ECS 2018 in Lisbon

2018/10/18

The H2020-CSA project "NanoElectronics Roadmap for Europe: Identification and Dissemination" (NEREID) will be present at the EF ECS 2018 in Lisbon from November 20-22.

At the Sinano booth, NEREID will distribute free copies of the final version of its **"NanoElectronics Roadmap for Europe"** ([Pre-final version available here](#)) while the roadmap is presented in three sessions at the **speaker's corner** on the 22nd and in the plenary session on 20th

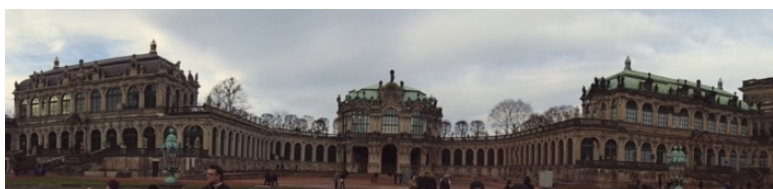


NEREID-SINANO-Workshop "Nanoelectronics and Smart System Technologies for Future applications"

2018/06/01

Embedded in the ESSDERC/ESSCIRC 2018 conference, in Dresden, Germany, NEREID had a workshop on 3/09/2018 to report and exchange on its recent findings on the way to map the future of European Nanoelectronics. The program is available below.





Topic: “Nanoelectronics and Smart System Technologies for Future applications”

3rd NEREID General Workshop

2018/02/16

Schedule:





June 14-15, 2018
directly after the
FTP-Workshop
on June 11-14, 2018

Location:

Hotel Pullman Timi Ama,
viale dei Ginepri 3
Villasimius
(close to Cagliari, Sardegna)



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First version of the NEREID roadmap has been introduced at EF ECS

2017/11/24

The H2020-CSA project “NanoElectronics Roadmap for Europe: Identification and Dissemination” (NEREID) has presented the first version of its roadmap (available at <https://www.nereid-h2020.eu/roadmap>) in the Speakers Corner during EF ECS. The slides are available [here](#).



WP3, WP4 & WP5 Cross-Domain-Workshops

2017/10/04

UPC Barcelona will be the location for a series of Cross Domain Workshops of the Work packages WP3, WP4 and WP5 of the NEREID project (“NanoElectronics Roadmap for Europe: Identification and Dissemination”) It will take place in **December 13-15, 2017**.



NEREID-SINANO-Workshop "Towards a new NanoElectronics Roadmap for Europe"

2017/05/31

Embedded in the **ESSDERC/ESSCIRC 2017 conference**, in Leuven, Belgium, NEREID will have a workshop on 9/11/2017 to report and exchange on its recent findings on the way to map the future of European Nanoelectronics. The **program** is available already.



WP2 Domain-Workshop on Alternative Computing Paradigms

2017/04/10

The second Domain Workshop of WP2 in NEREID (“NanoElectronics Roadmap for Europe: Identification and Dissemination”) will be about Alternative Computing Paradigms and will take place in May 11-12, 2017, in Sitges, near Barcelona.

Topic: “Non-conventional information processing and computing paradigms approaches and devices”.



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Program (version 6 from 5/8/17)

Information for speakers

2nd General Workshop

2016/06/17

Schedule:

April 6-7, 2017

directly after the

EUROSOI-ULIS Conference

on April 3-5, 2017

Location:

Divani Caravel Hotel

Vassileos Alexandrou 2,

16121 Athens



Upload:

List of suggested hotels

Leaflet of General Workshop

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