



Published on NEREID (<https://www.nereid-h2020.eu>)

[Home](#) > [Printer-friendly PDF](#)

NEREID presents its final Roadmap

2018/12/14

[Press Releases](#) [1]

Subtitle:

The H2020 project NEREID finished and released its final result: The NEREID roadmap on the future of European Nanoelectronics

Images



Teaser Image:



Quelle

Publishing Date:

Fri, 2018/12/14

Hannover ... December 14, 2018: The NEREID project (“NanoElectronics Roadmap for Europe: Identification and Dissemination”) has introduced the final version of its roadmap on the future of European nanoelectronics on November 21, 2018, at the EFFECS 2018 in Lisbon. This announced final version of the NEREID roadmap is now published at <https://www.nereid-h2020.eu/roadmap> [2].


“The distinguishing characteristic of the project is to bring together technology and market experts, jointly defining technology roadmaps starting from application needs.” says Dr Patrick Cogež from AENEAS and industrial co-leader of the project. Following this approach, a series of workshops has involved more than 100 international research and application experts, coming from various institutions out of eleven countries.

“The objective of NEREID is to elaborate a new roadmap for nanoelectronics, focused on the requirements of European semiconductor and application industries.” says Dr Francis Balestra of Grenoble INP/CNRS and coordinator of the project. “It addressed societal challenges following advanced concepts developed by Research Centers and Universities in order to achieve an early identification of promising novel technologies covering the R&D needs all along the innovation chain. The final result is a roadmap for European micro- and nanoelectronics, with a clear identification of objectives from medium to long term.”

The NEREID roadmap for nanoelectronics is intended as input for future research programmes at European and National levels. It aims to help coordinating efforts to solve the main challenges in nanoelectronics and put the EU at the forefront of future technological developments. Therefore it takes into account the specificity of the European industrial and academic landscape, and targets a better coordination between academic and industrial research in equipment, semiconductors and application developments.

The NEREID roadmap is divided into several main technology sectors: Advanced Logic (including Nanoscale FETs and Memories) and Connectivity, Functional Diversification (Smart Sensors, Smart Energy, Energy for Autonomous Systems), Beyond-CMOS (Emerging Devices and Computing Paradigms), Heterogeneous Integration and System Design, Equipment, Materials and Manufacturing Science, and it also includes cross-functional enabling domains.

Herunterladen:

 [NEREID Flyer](#) [3]

The NEREID project (ICT-CSA-685559) is supported within the Research and Innovation Programme **Horizon 2020** by the **EU**.

Source URL: <https://www.nereid-h2020.eu/content/nereid-presents-its-final-roadmap-0>

Links:

[1] <https://www.nereid-h2020.eu/taxonomy/term/107>

[2] <https://www.nereid-h2020.eu/roadmap>

[3] https://www.nereid-h2020.eu/system/files/nereid_8_print.pdf