NEREID presents the pre-final NEREID Roadmap

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Press Releases [1]

Subtitle:

The H2020 project NEREID nearly finished and releases its prefinal roadmap on the future of European Nanoelectronics

Images

Teaser Image:

Quelle

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Dresden … September 3, 2018: The NEREID project (“NanoElectronics Roadmap for Europe: Identification and Dissemination”) has presented the pre-final version of its roadmap on the future of European nanoelectronics on September 3, 2018, at the NEREID/SInano-Workshop alligned with ESSCIRC/ESSDERC 2018 in Dresden. In order to enable a public discussion and to identify possible improvements this pre-final version of the NEREID roadmap has already been published at https://www.nereid-h2020.eu/roadmap [2].

“The distinguishing characteristic of the project is to bring together technology and market experts, jointly defining technology roadmaps starting from application needs.” says Dr Patrick Cogez from AENEAS and industrial co-leader of the project. Following this approach, a series of workshops has involved more than 100 international research and application experts, coming from various institutions out of eleven countries.

“The objective of NEREID is to elaborate a new roadmap for nanoelectronics, focused on the requirements of European semiconductor and application industries.” says Dr Francis Balestra from Grenoble INP and coordinator of the project. “It will address societal challenges following advanced concepts developed by Research Centres and Universities in order to achieve an early identification of promising novel technologies covering the R&D needs all along the innovation chain. The final result will be a roadmap for European micro- and nanoelectronics, with a clear identification of objectives from medium to long term.”

The NEREID roadmap for nanoelectronics is intended as input for future research programmes at European and National levels. It aims to help coordinating efforts to solve the main challenges in nanoelectronics and put the EU at the forefront of future technological developments. Therefore it takes into account the specificity of the European industrial and academic landscape, and targets a better coordination between academic and industrial research in equipment, semiconductors and application developments.

The NEREID roadmap is divided into several main technology sectors: Advanced Logic (including Nanoscale FETs and Memories) and Connectivity, Functional Diversification (Smart Sensors, Smart Energy, Energy for Autonomous Systems), Beyond-CMOS (Emerging Devices and Computing Paradigms), Heterogeneous Integration and System Design, Equipment, Materials and Manufacturing Science, and it also includes cross-functional enabling domains.

Herunterladen:

NEREID Flyer [3]

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Links:
[1] https://www.nereid-h2020.eu/taxonomy/term/107
[2] https://www.nereid-h2020.eu/roadmap
[3] https://www.nereid-h2020.eu/system/files/nereid_8_print.pdf